

10651

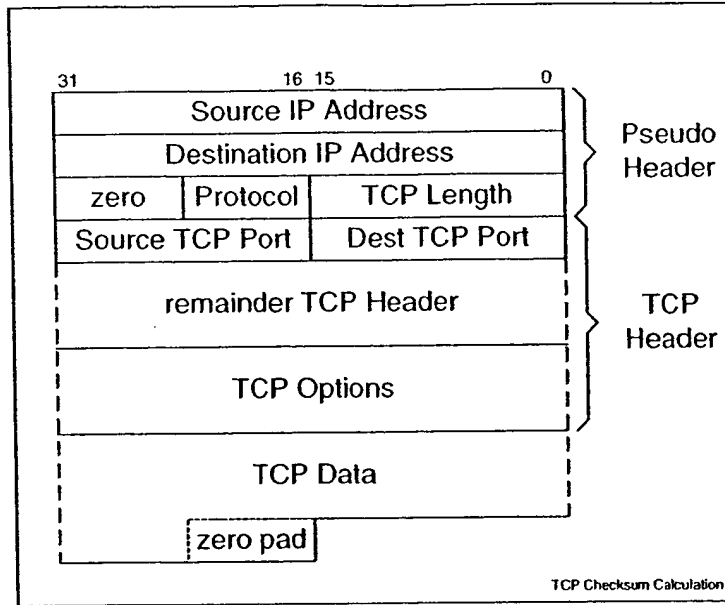


Figure 1A (PRIOR ART)

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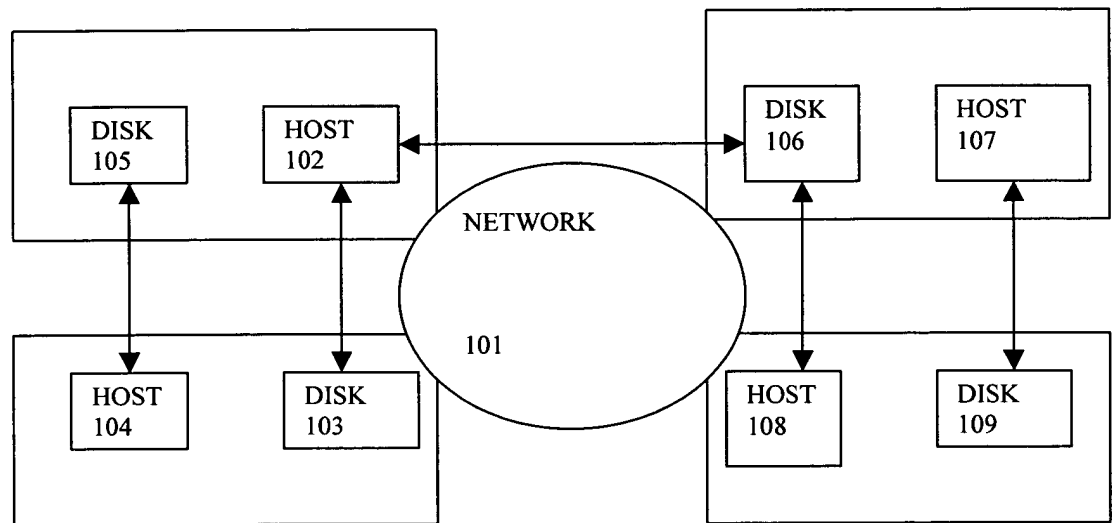


FIGURE 2A

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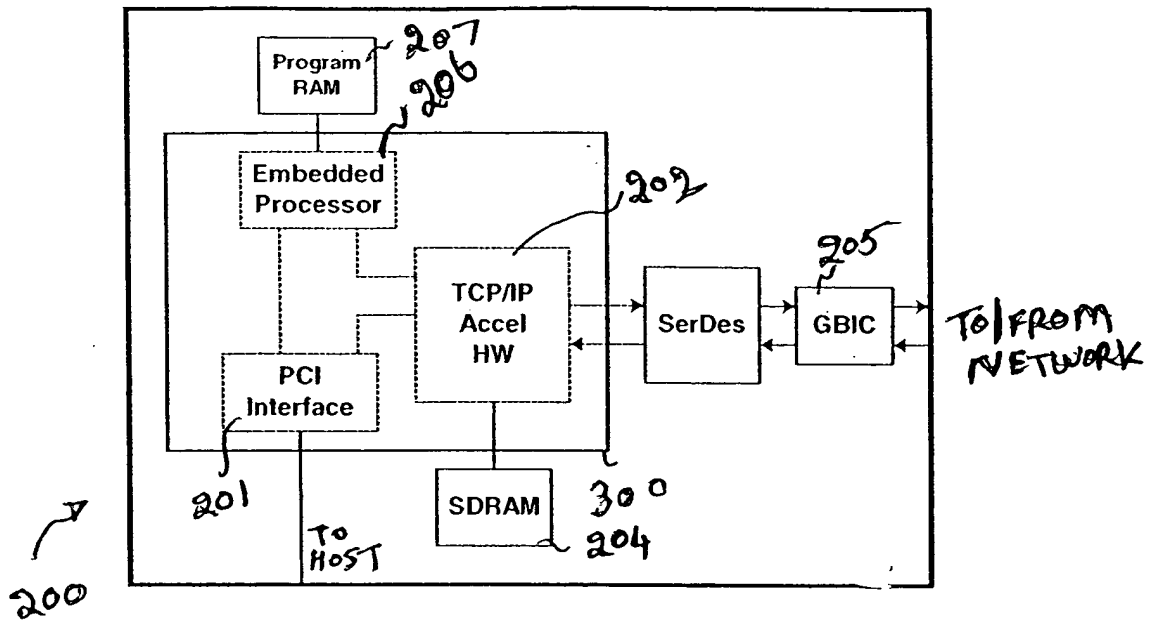


Figure 2B

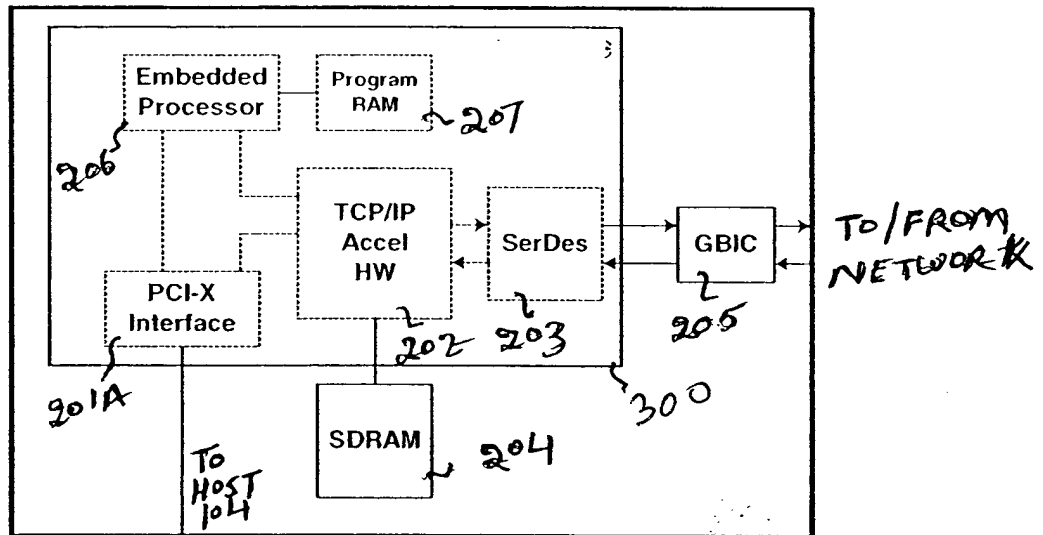


Figure 2C

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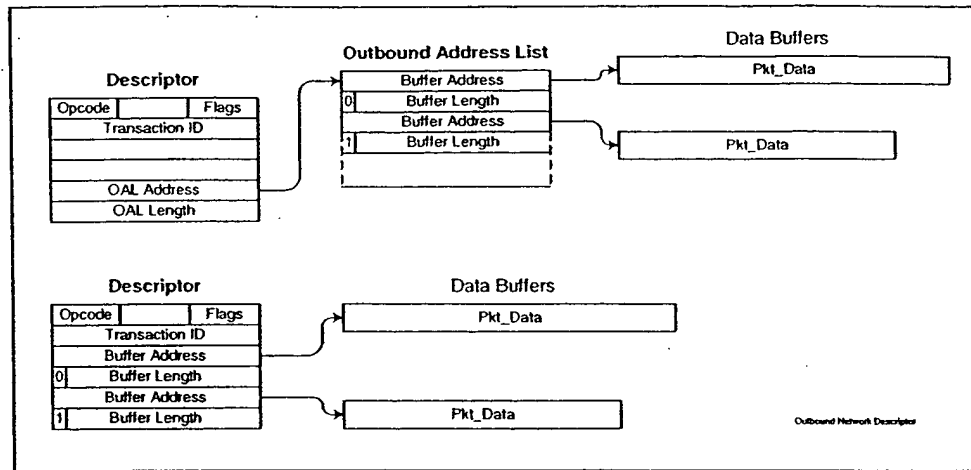


Figure 3A-1 Network IOCB Example

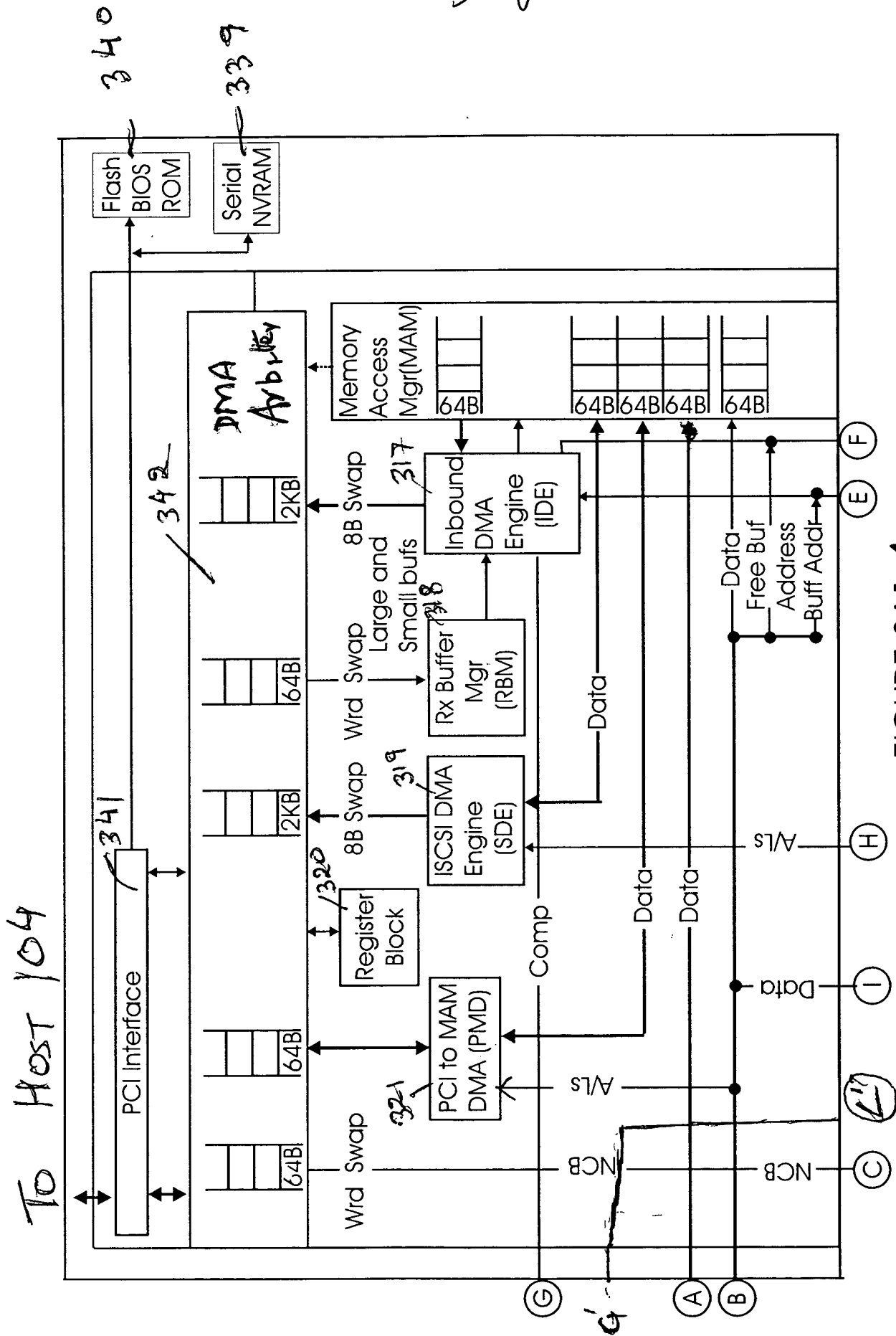
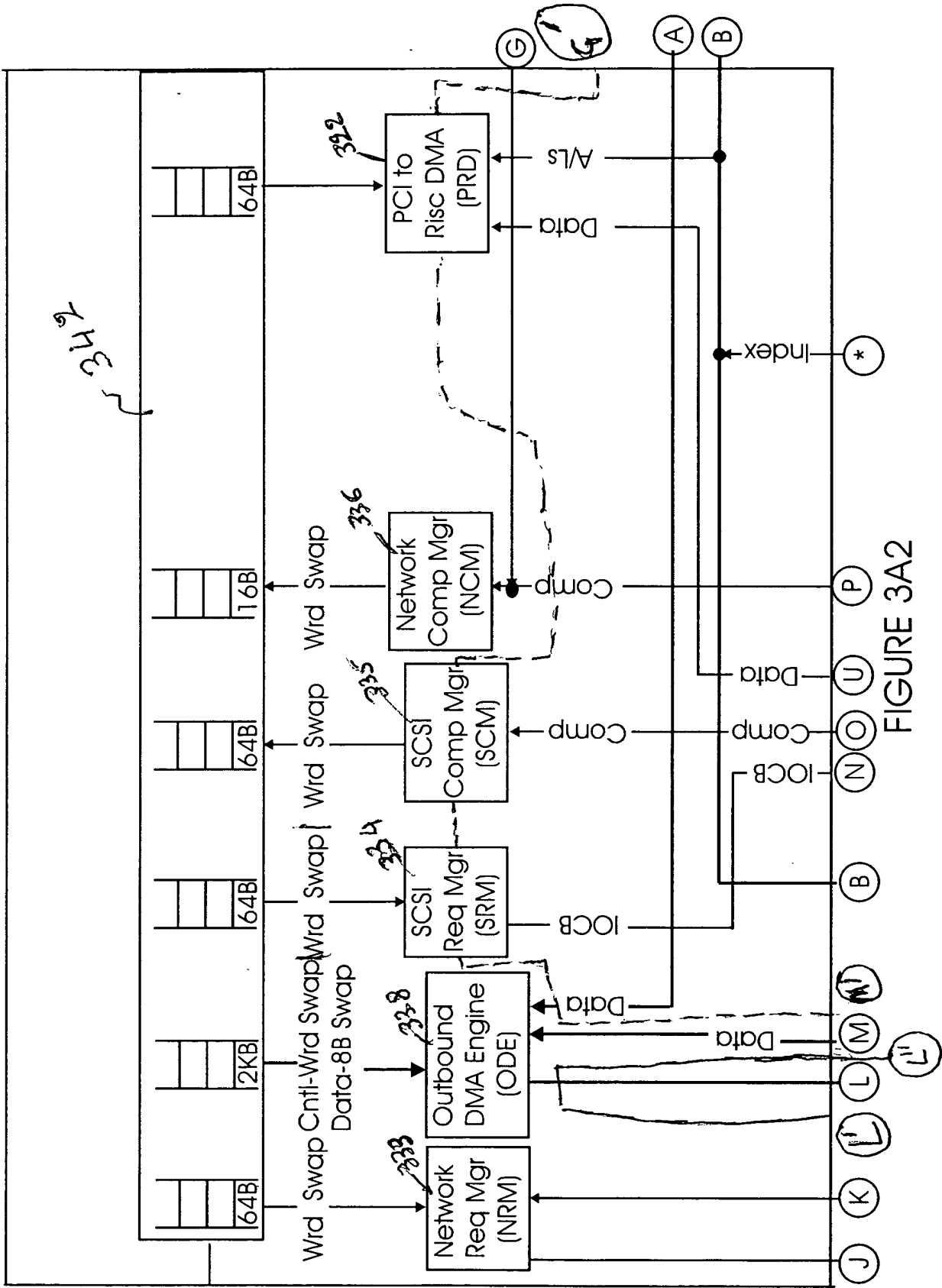


FIGURE 3A1-1



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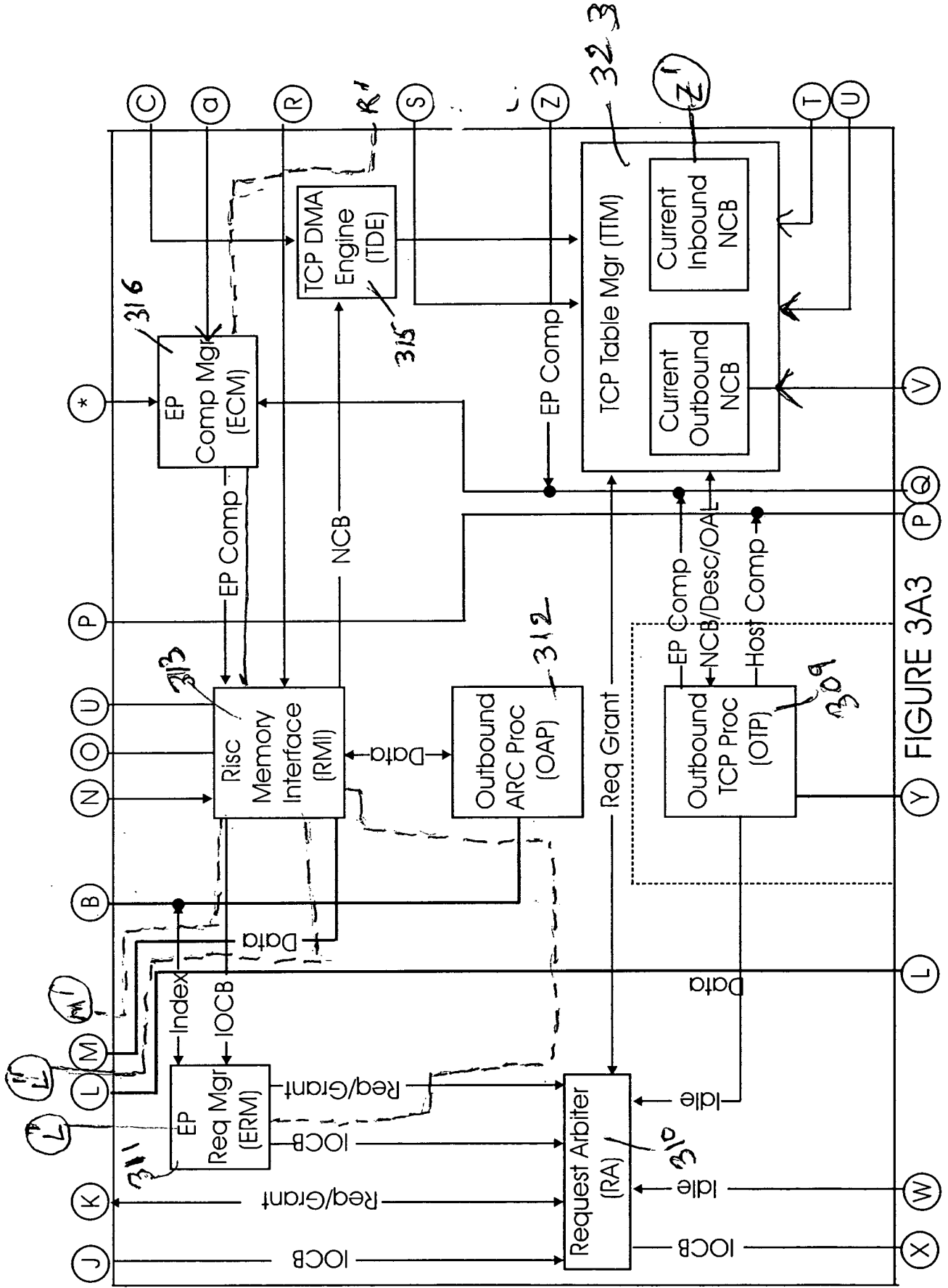
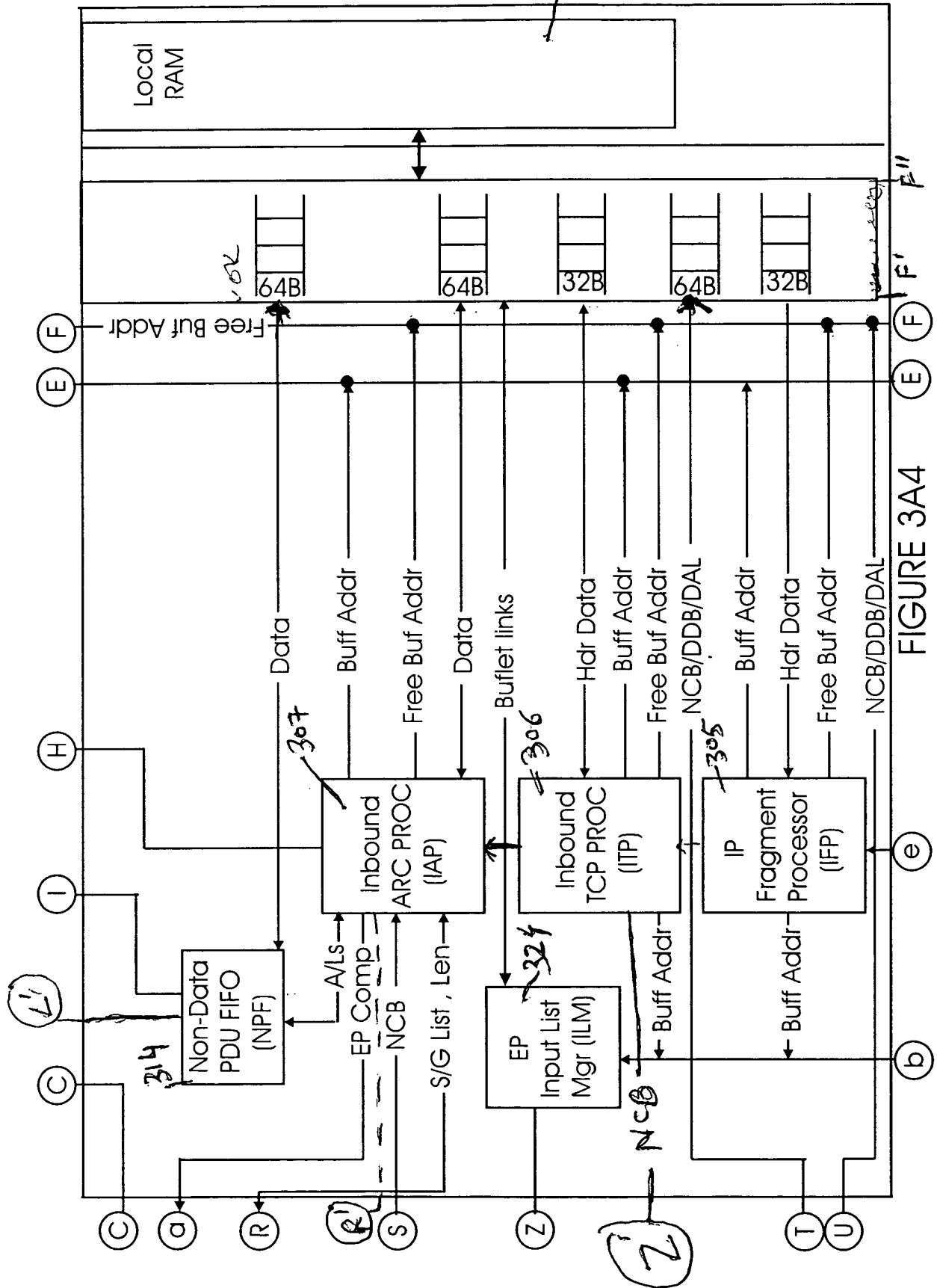
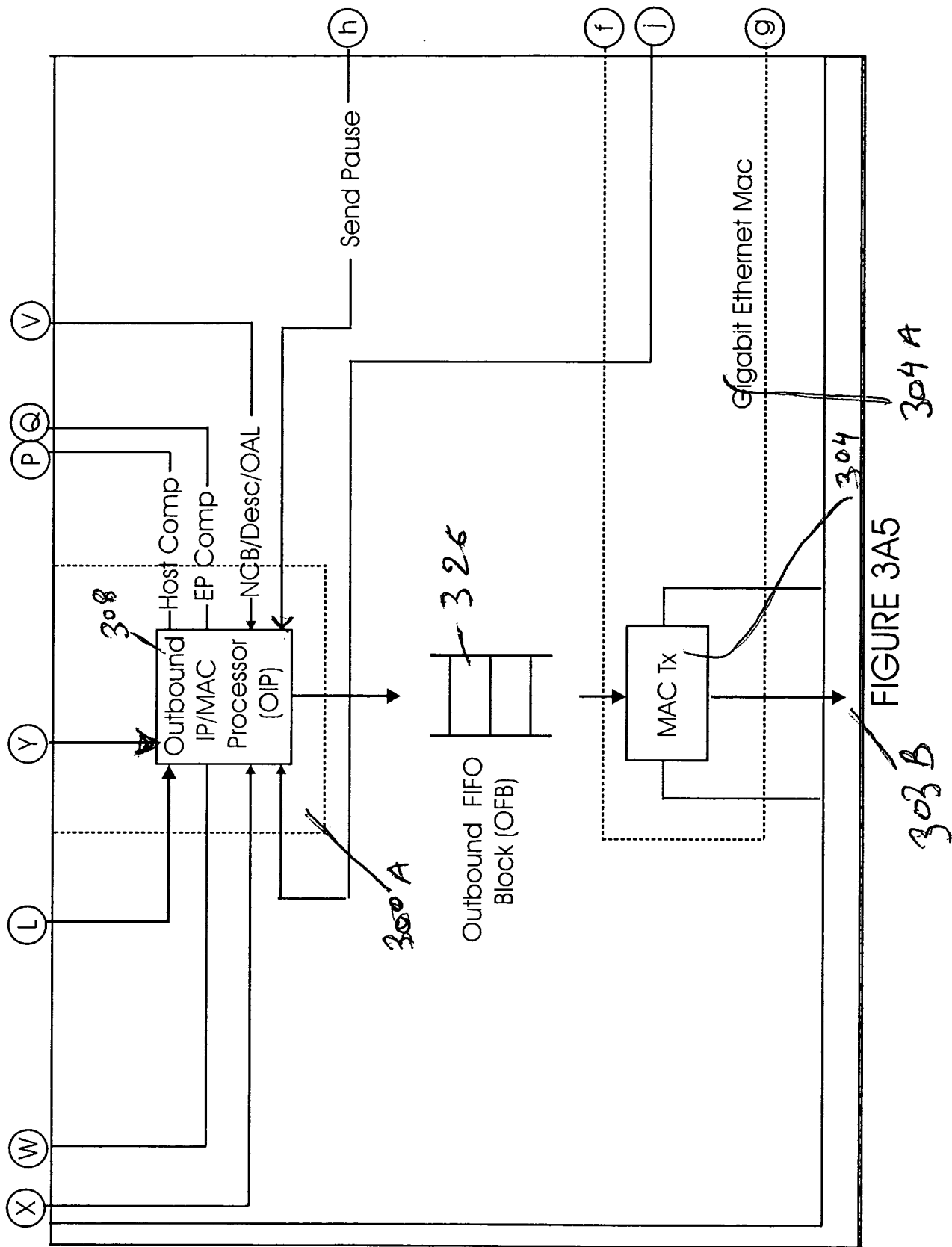


FIGURE 3A3





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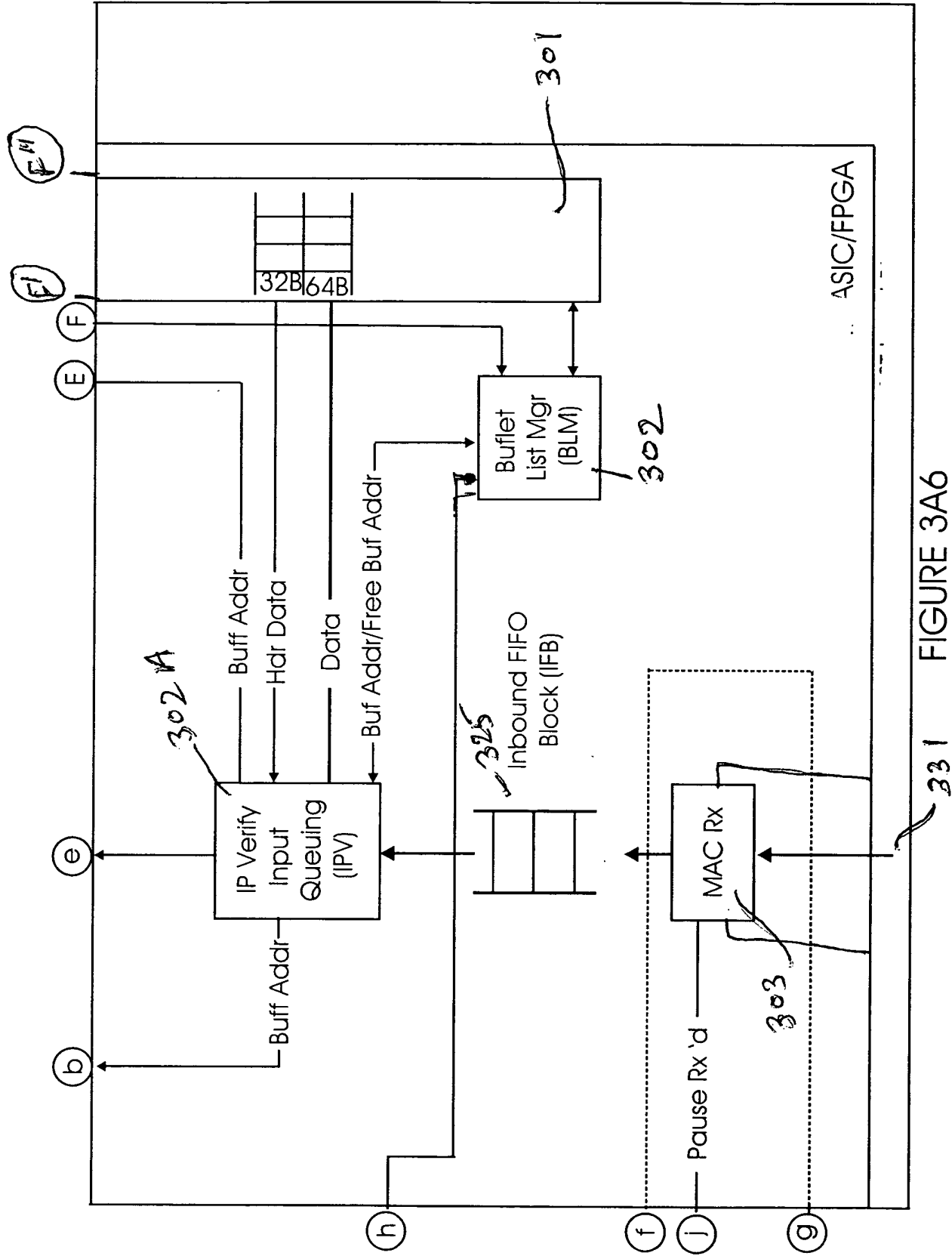


FIGURE 3A6

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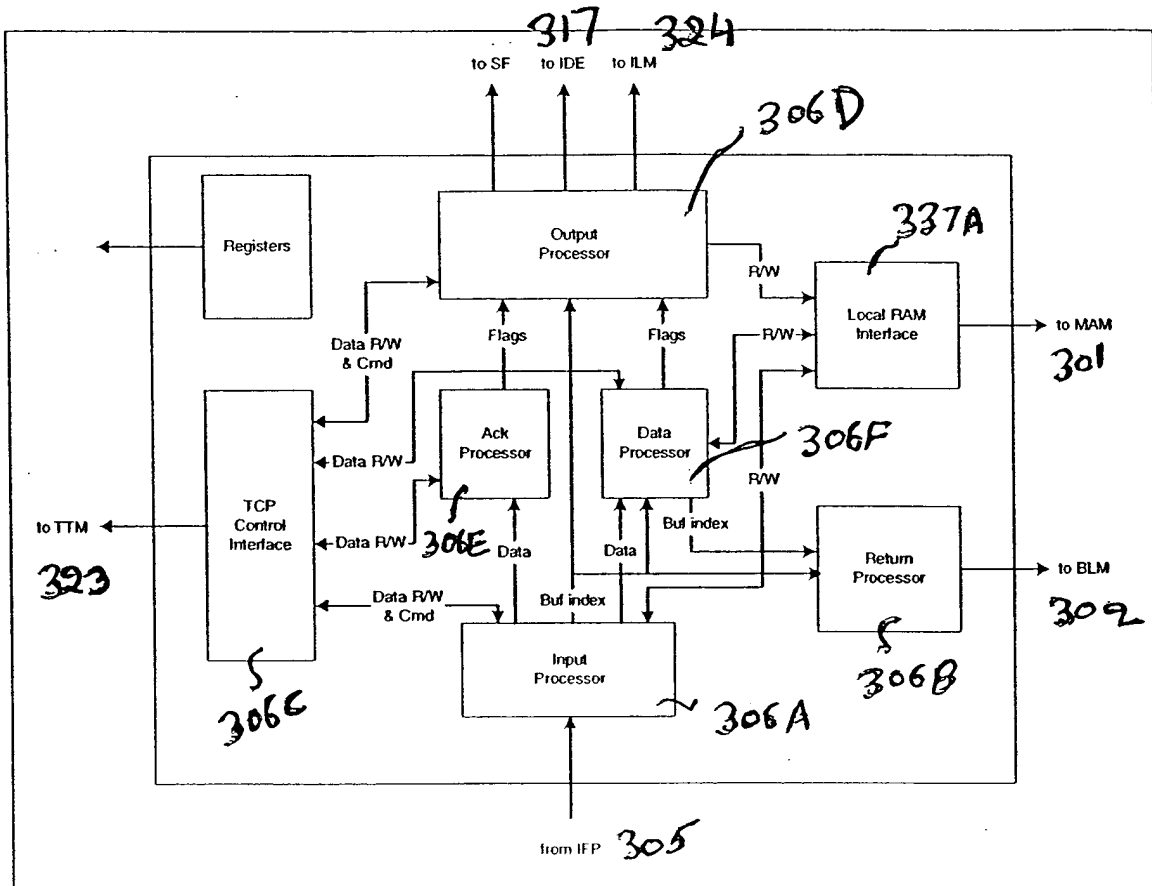


Figure 3B -

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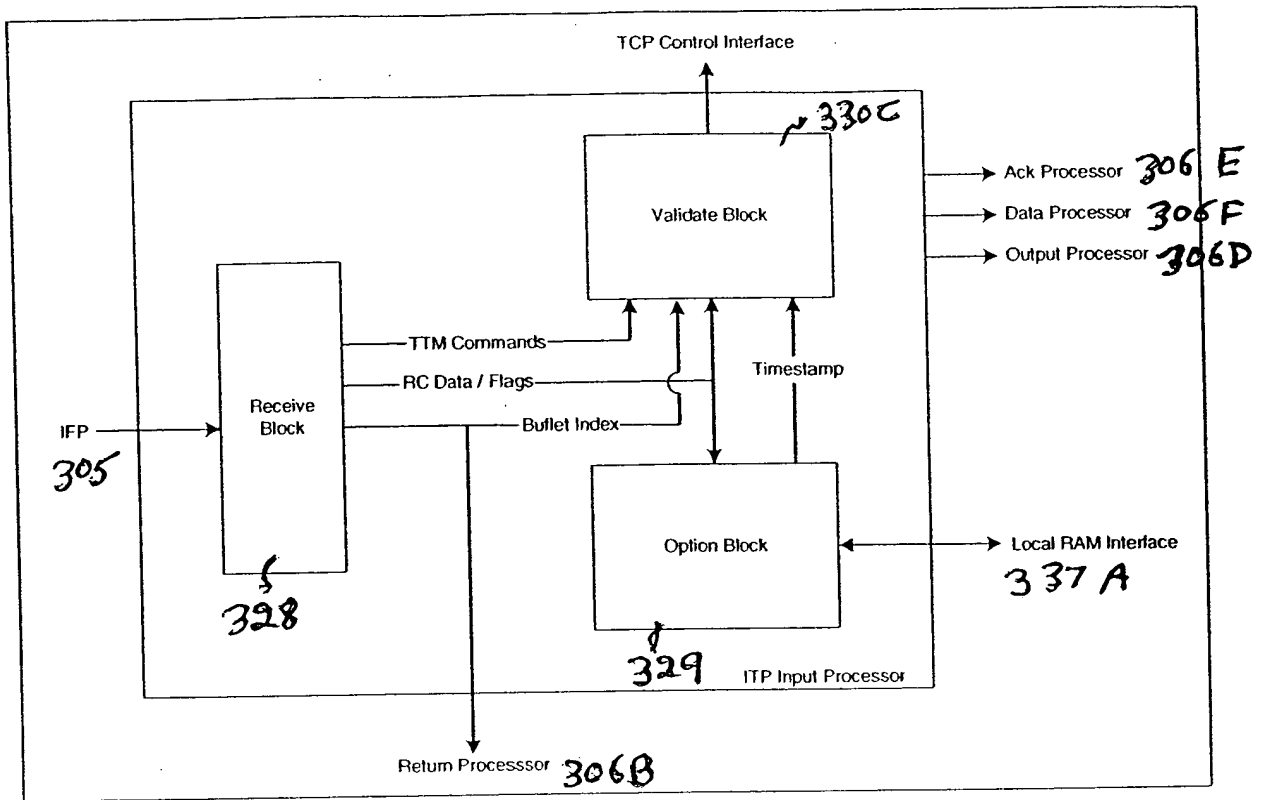


Figure 3C – ITP Input Processor

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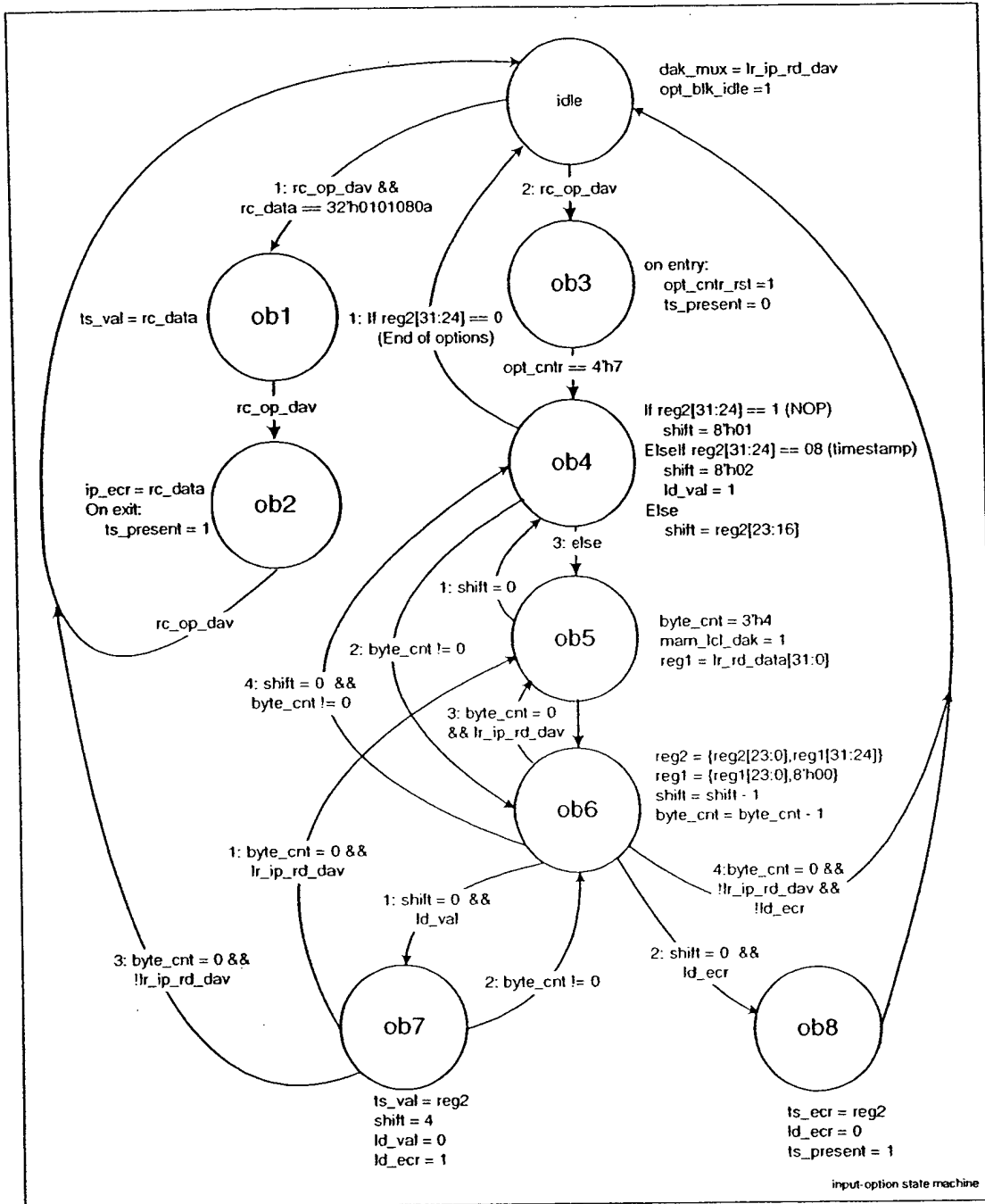


Figure 3C1 Input Processor Option Block State Machine

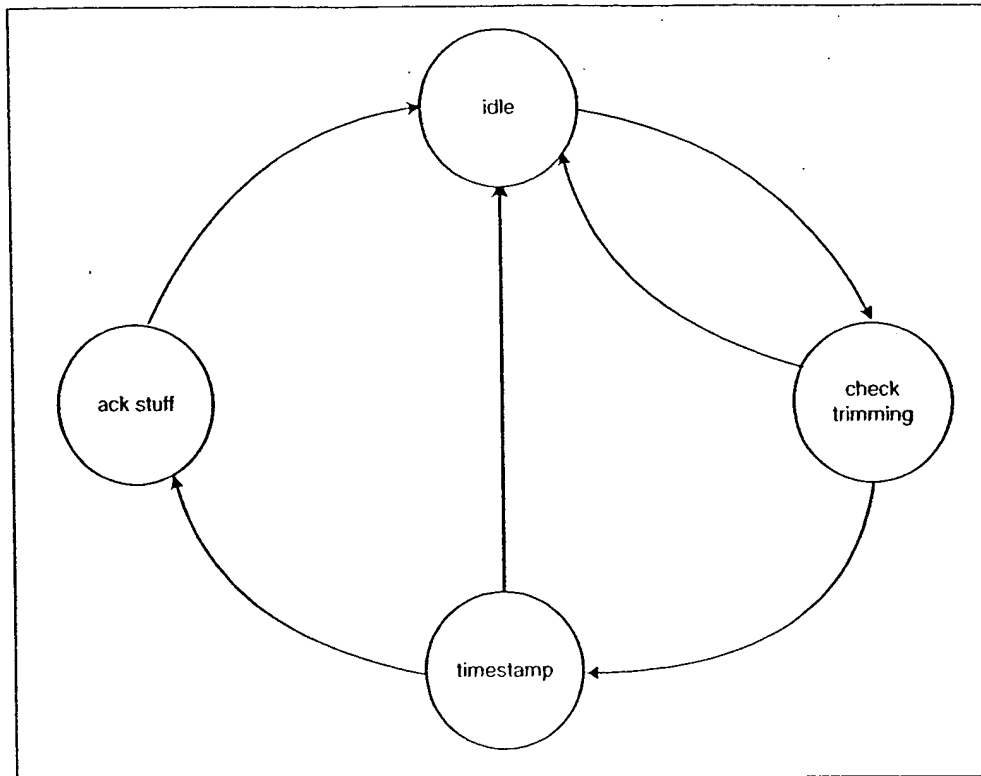


Figure 3C2: Validation State Machine

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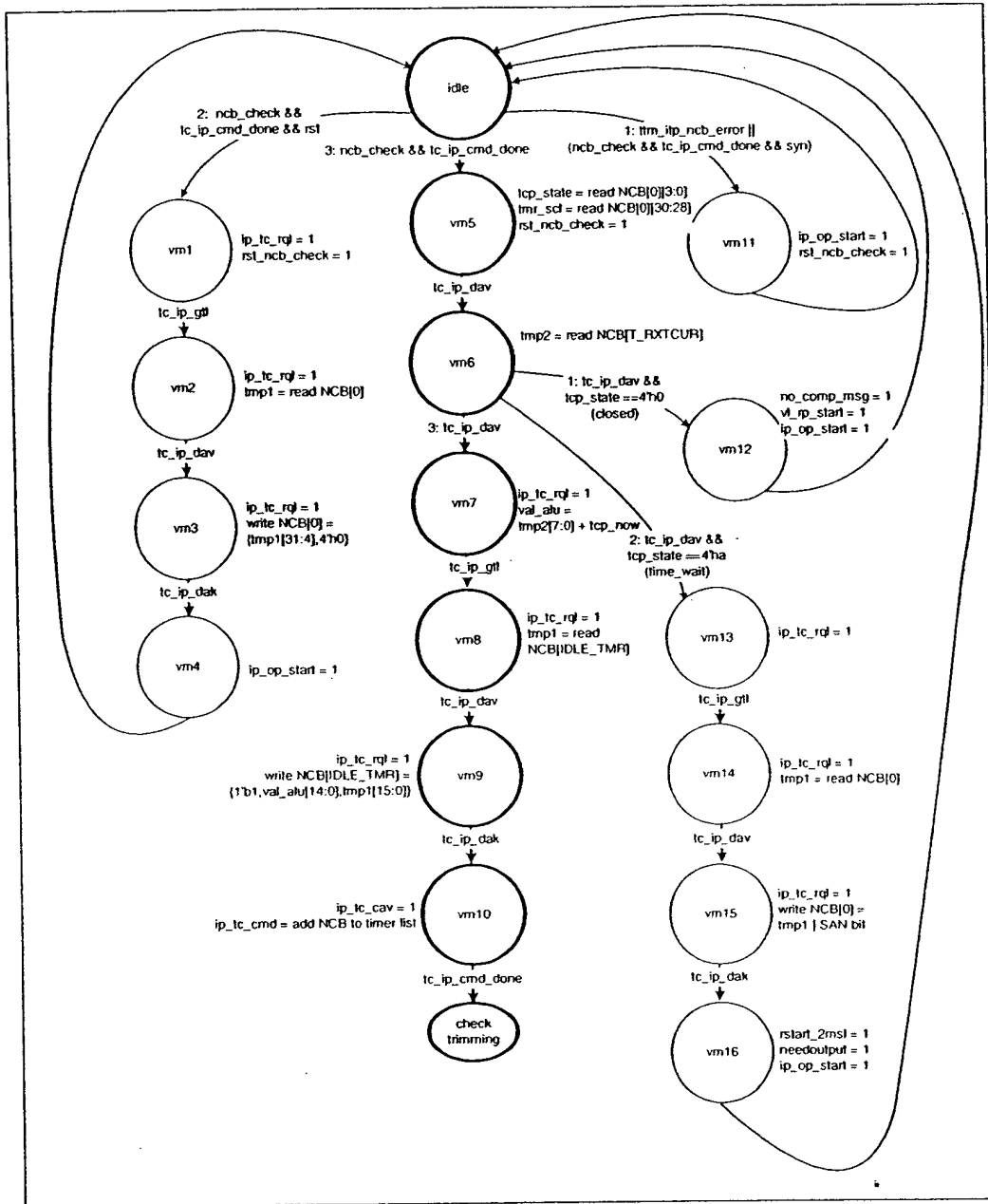


Figure 3C3: Validation State Machine for Reset, SYN or Invalid State

16 ab 51

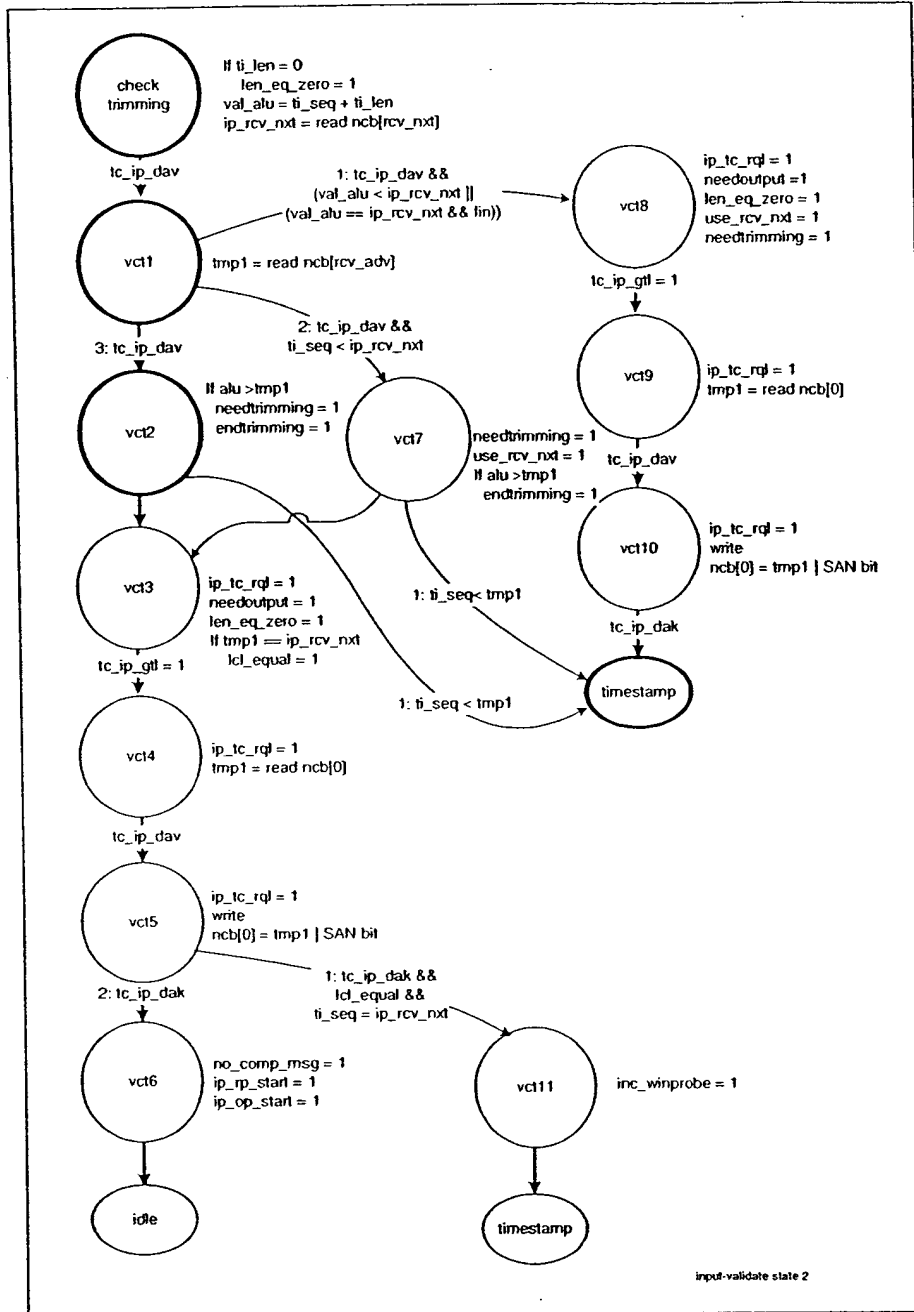


Figure 3C4: Validation State Machine for Trimming



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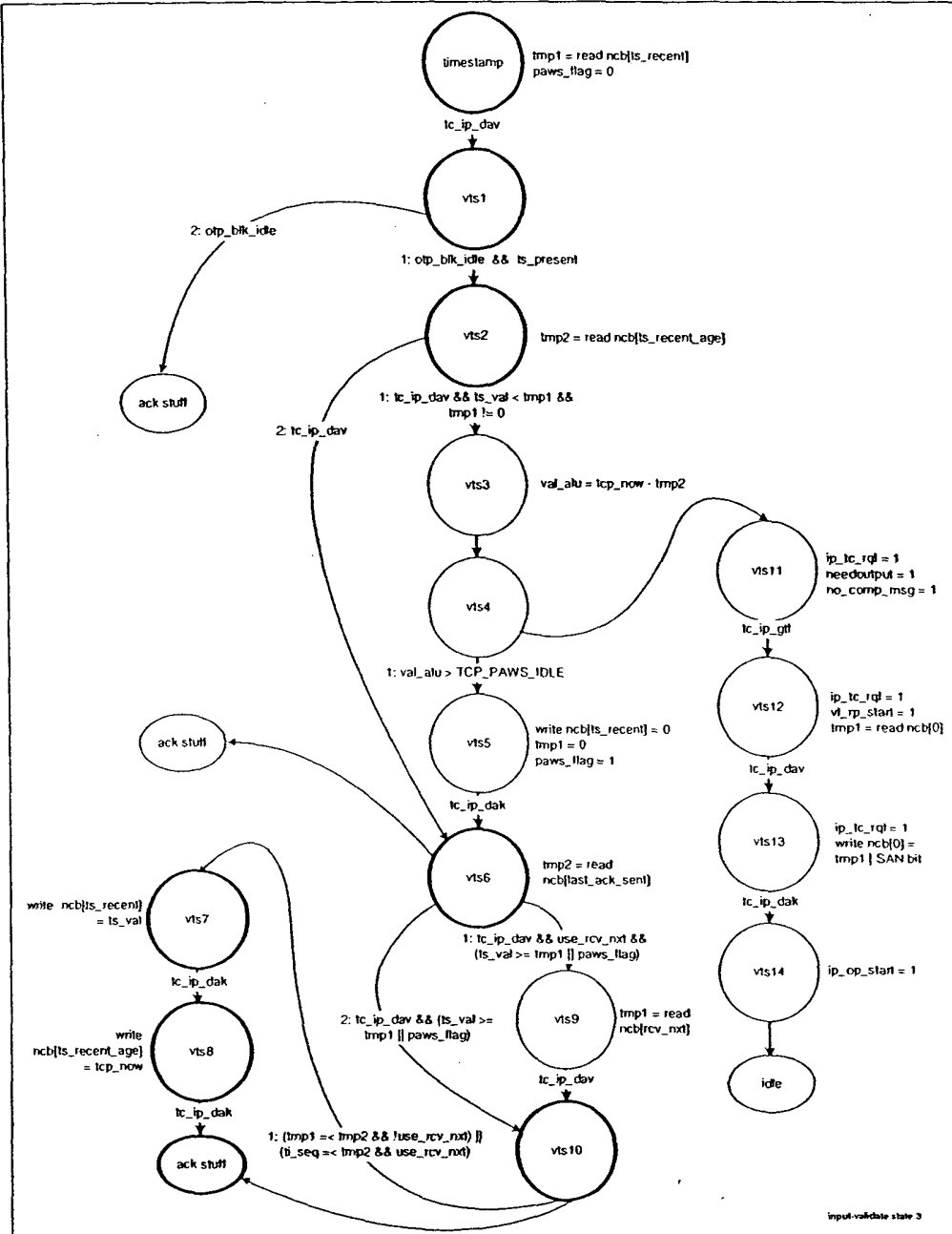


Figure 3C5: Validation State machine for Timestamp functionality

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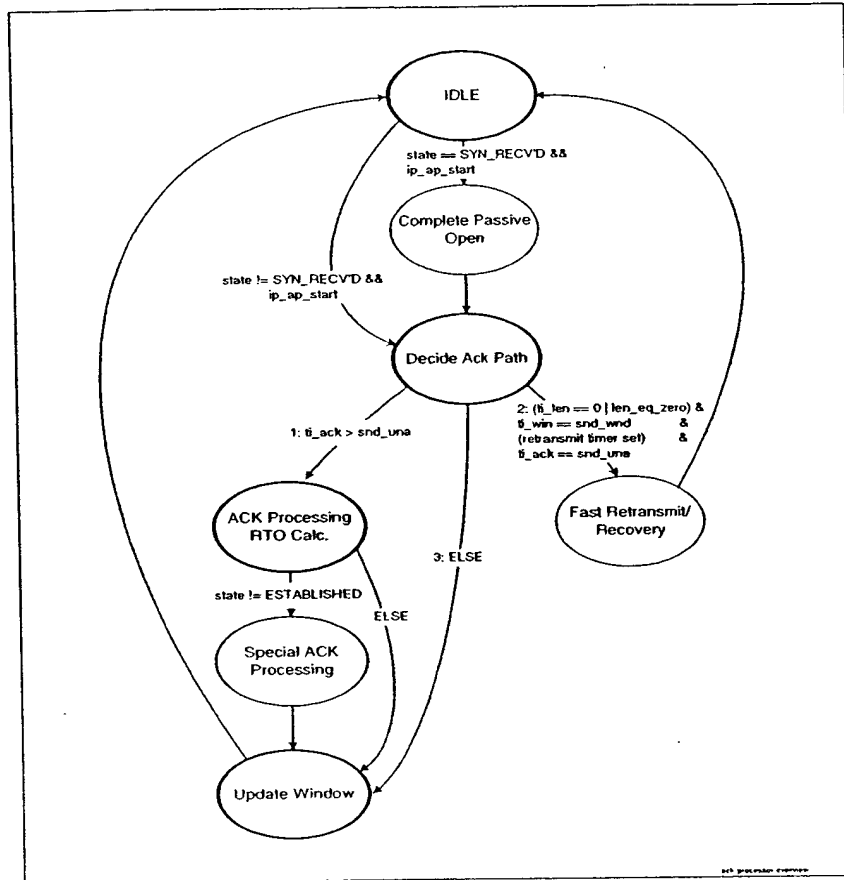


Figure3C6- ACK Processor

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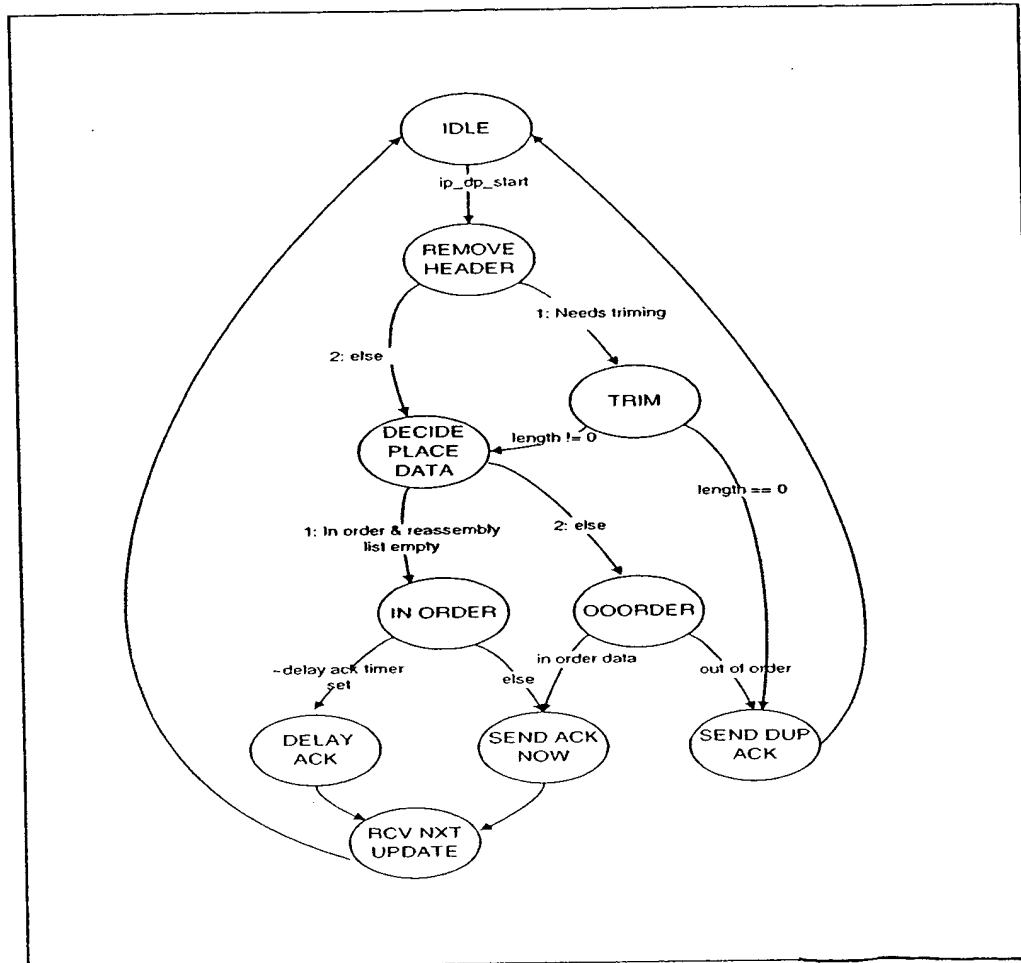


Figure 3C7 Data Processing State Machine

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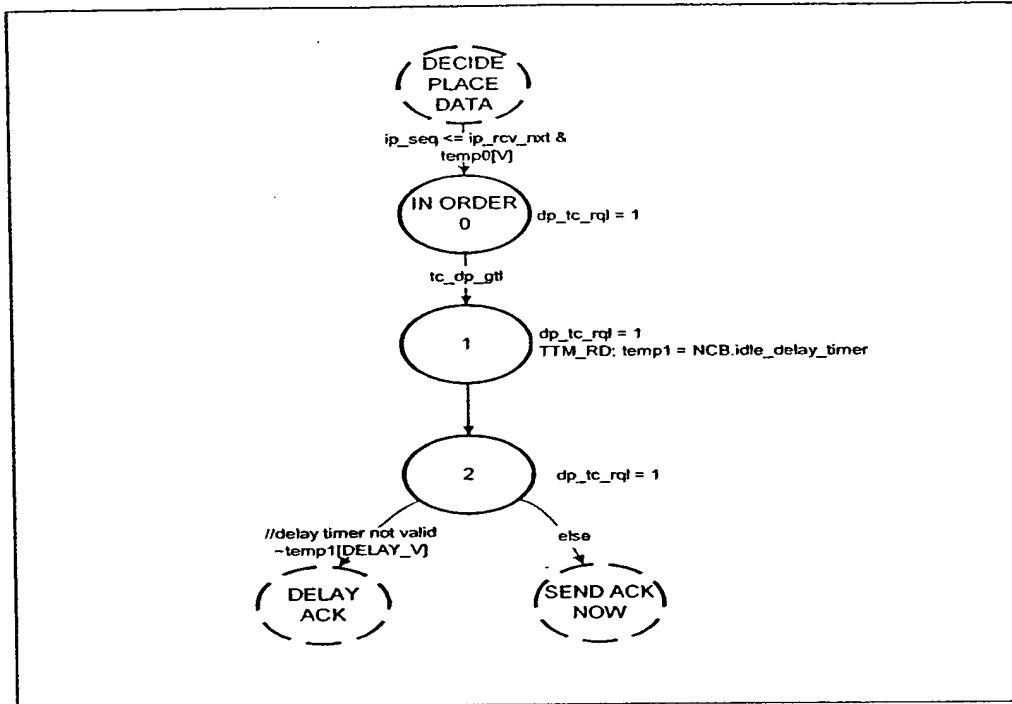


Figure 3C8 – Data Process Inorder

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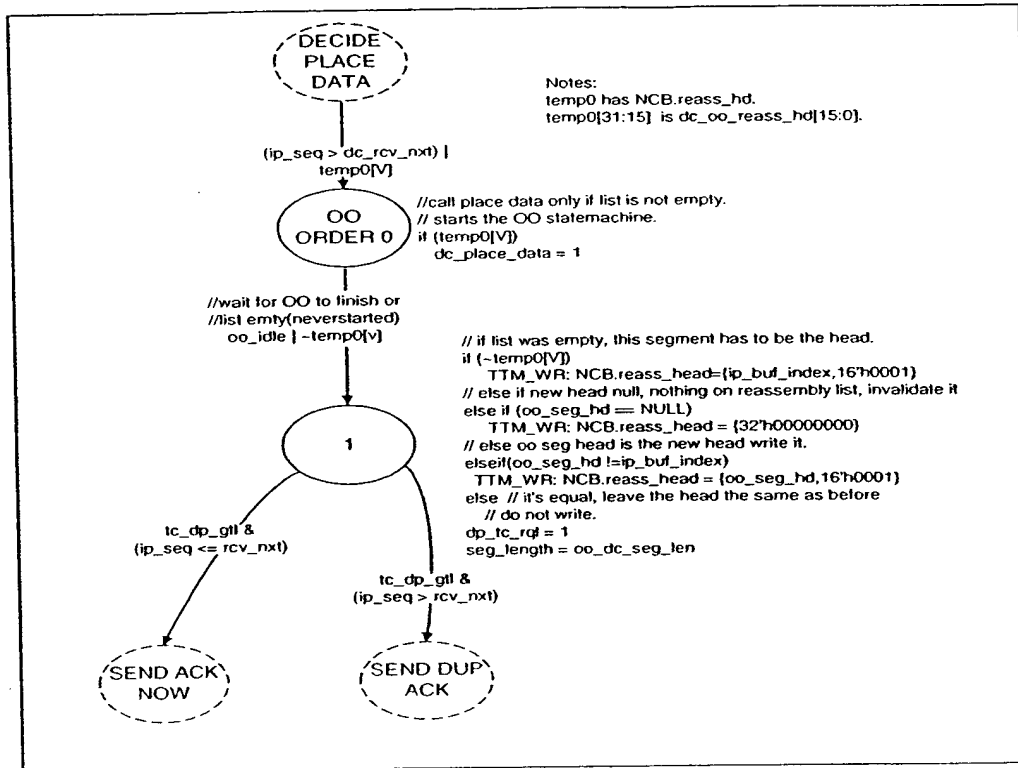


Figure 3C9 – Data Process Out of Order

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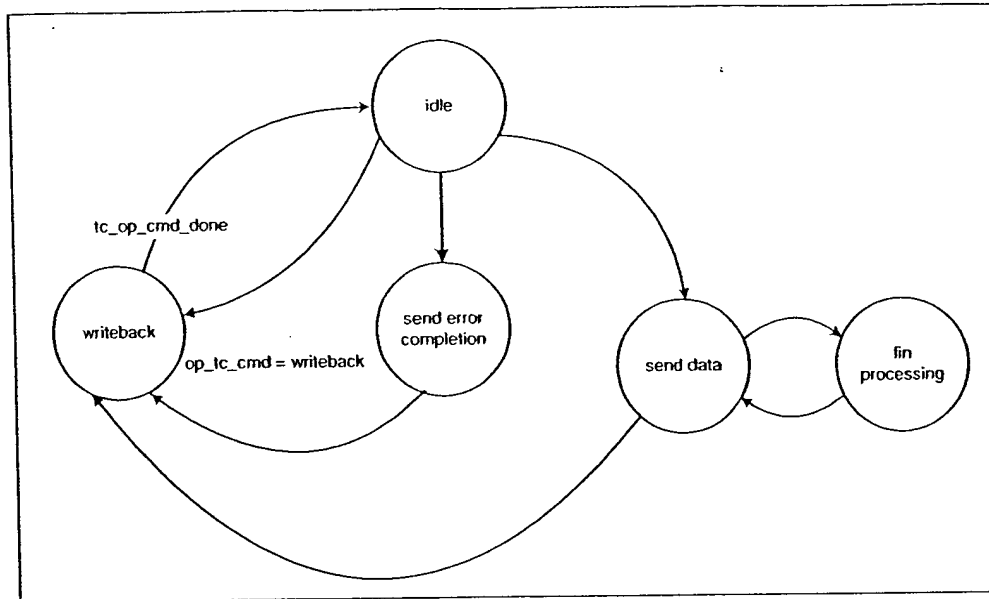


Figure 3C10 - ITP Output Processor

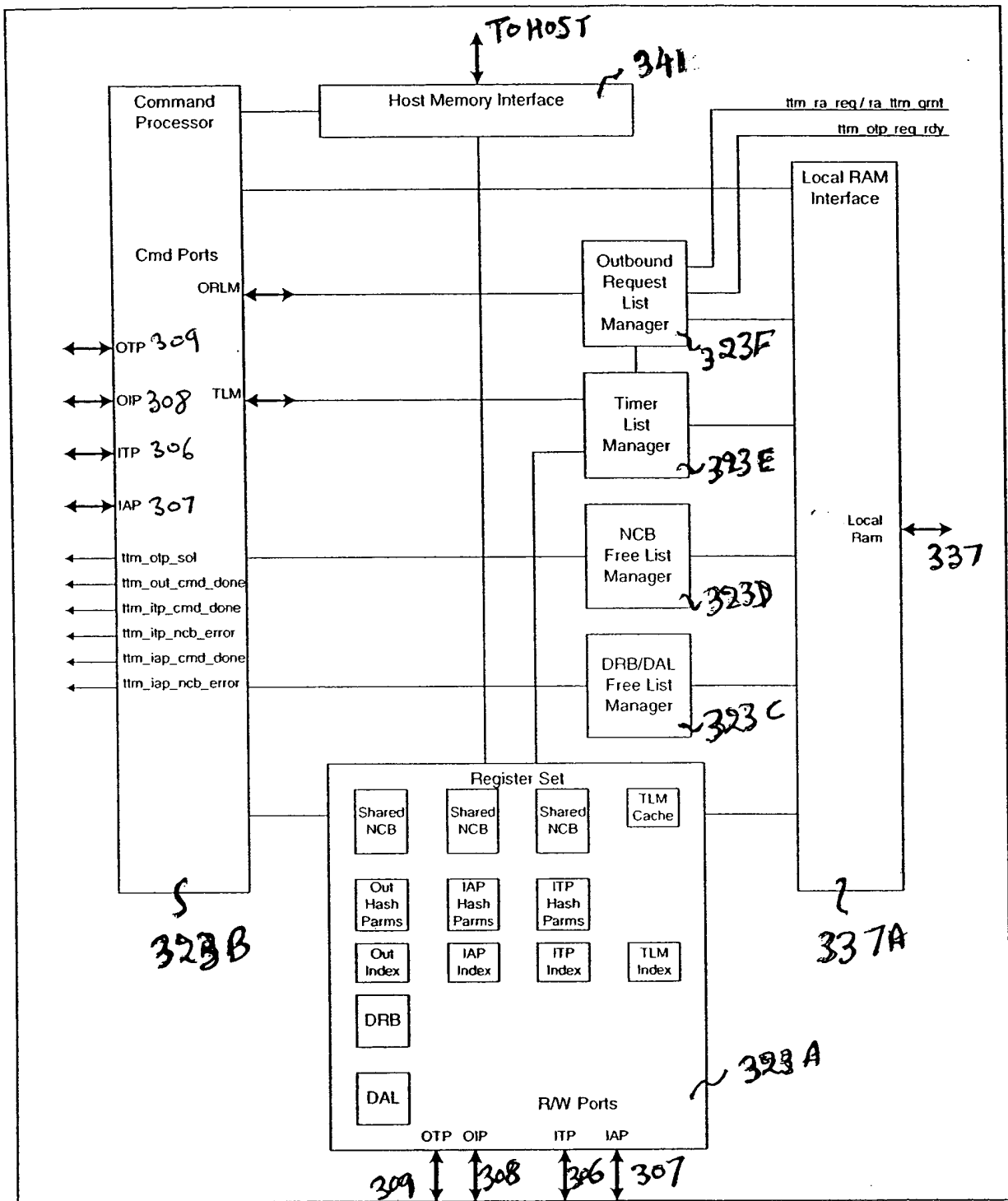


Figure 3D 1

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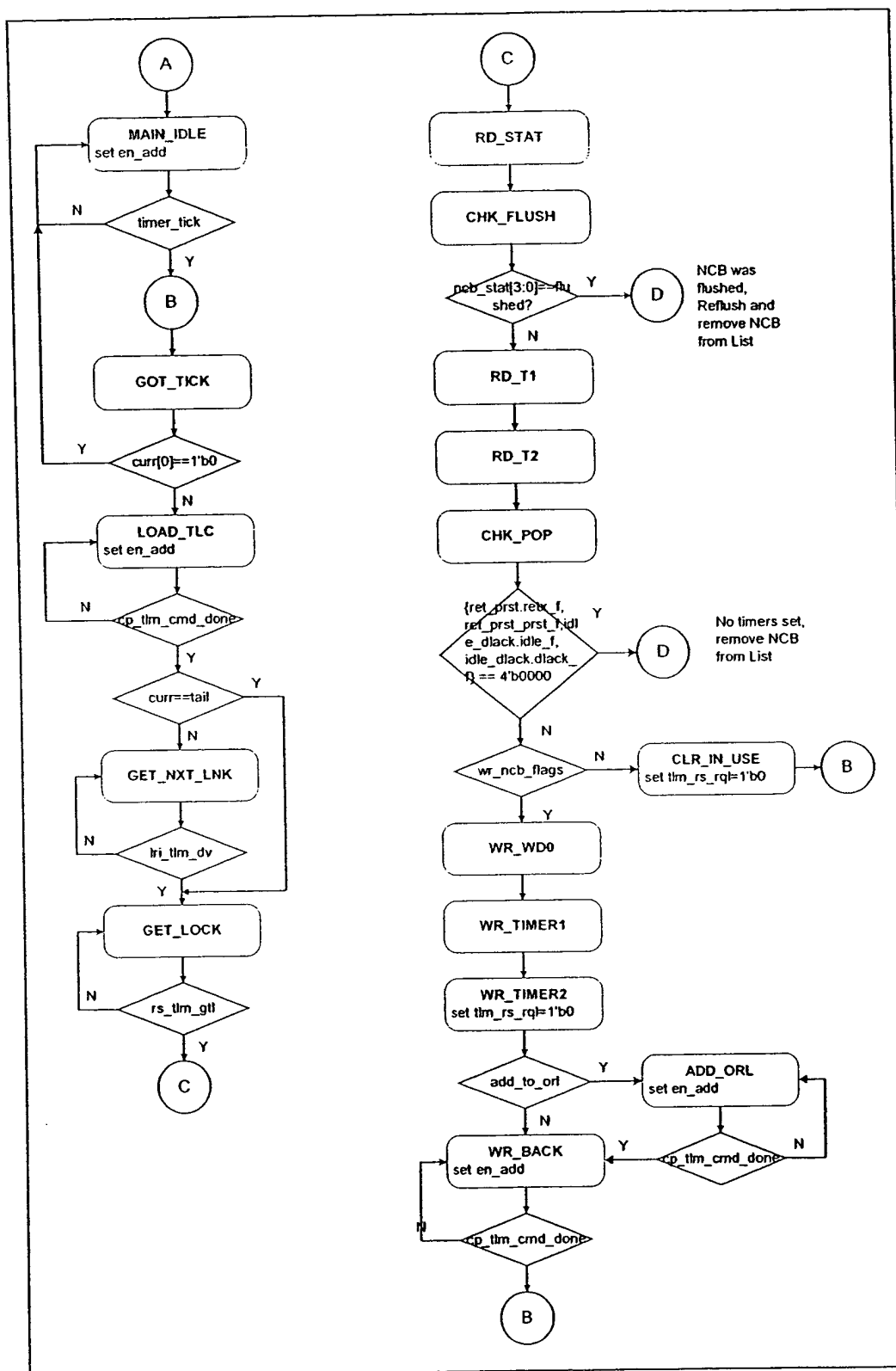


Figure 3D1 Timer List Manager State Machine



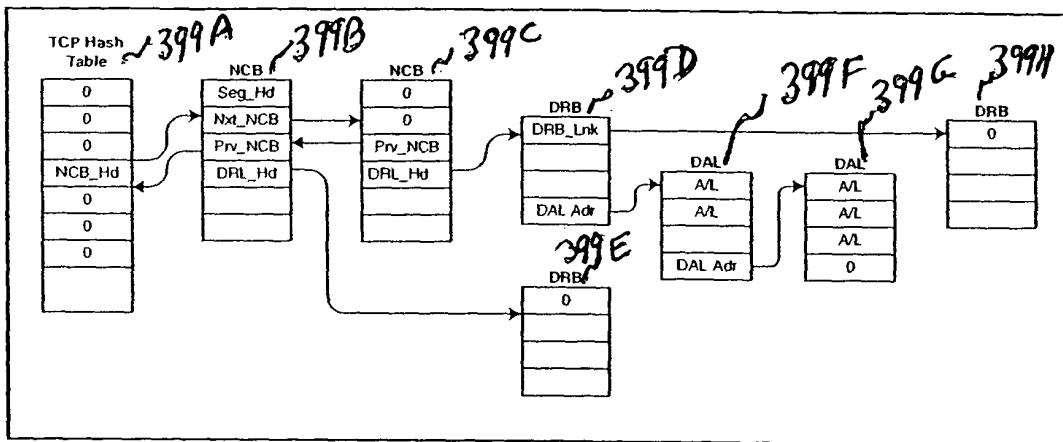


Figure 3E

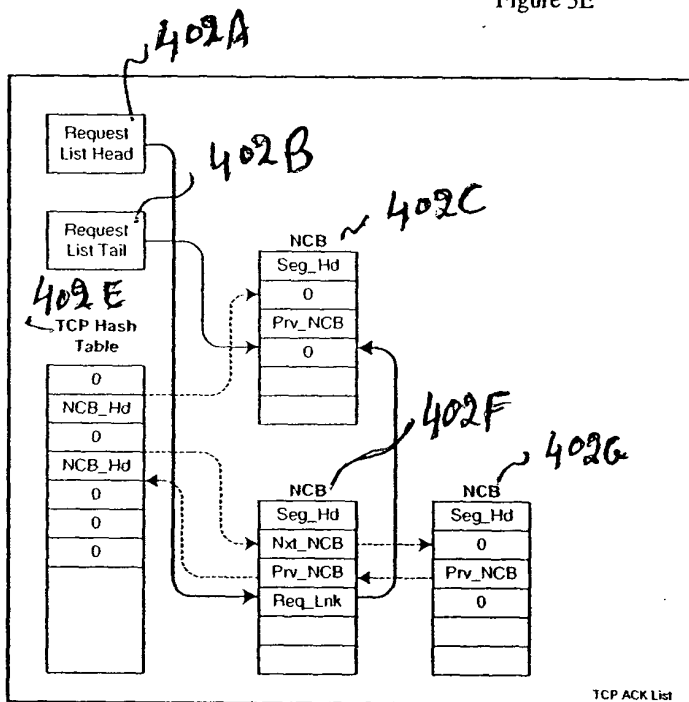


Figure 3F

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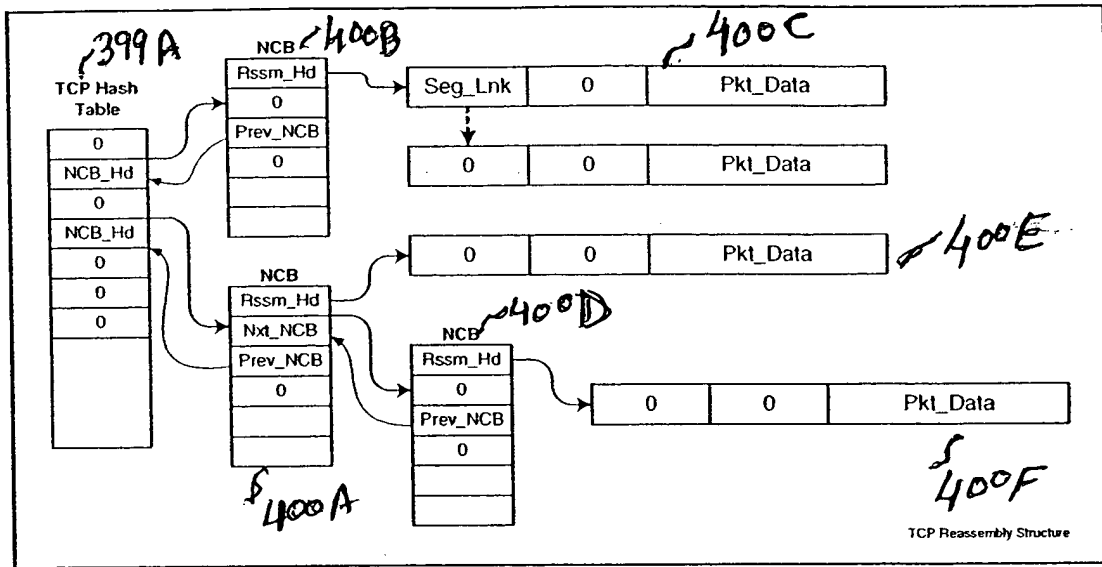


Figure 3G

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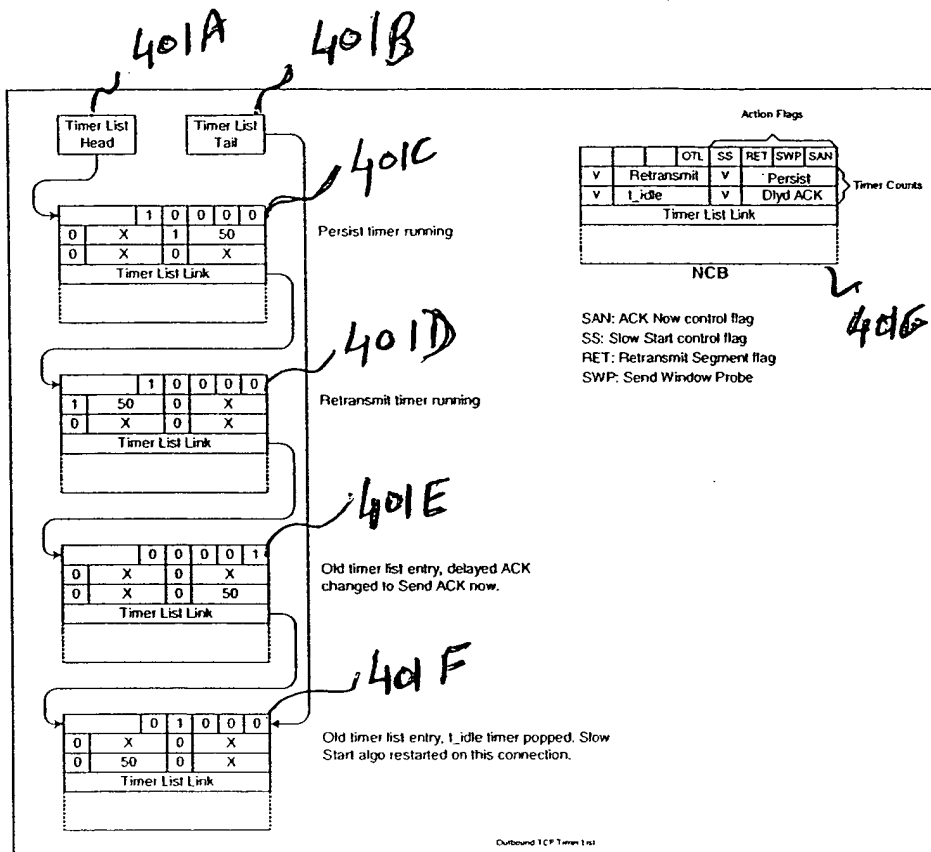


Figure 3H

401

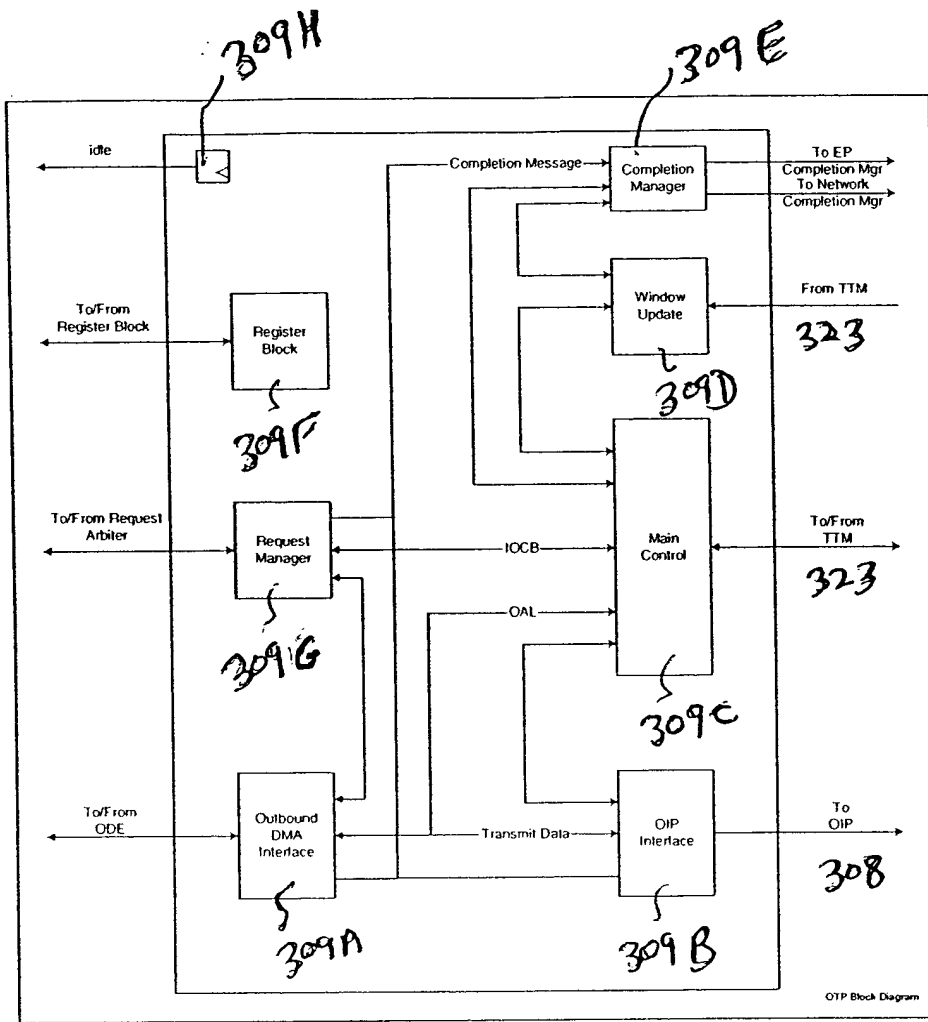


Figure 31

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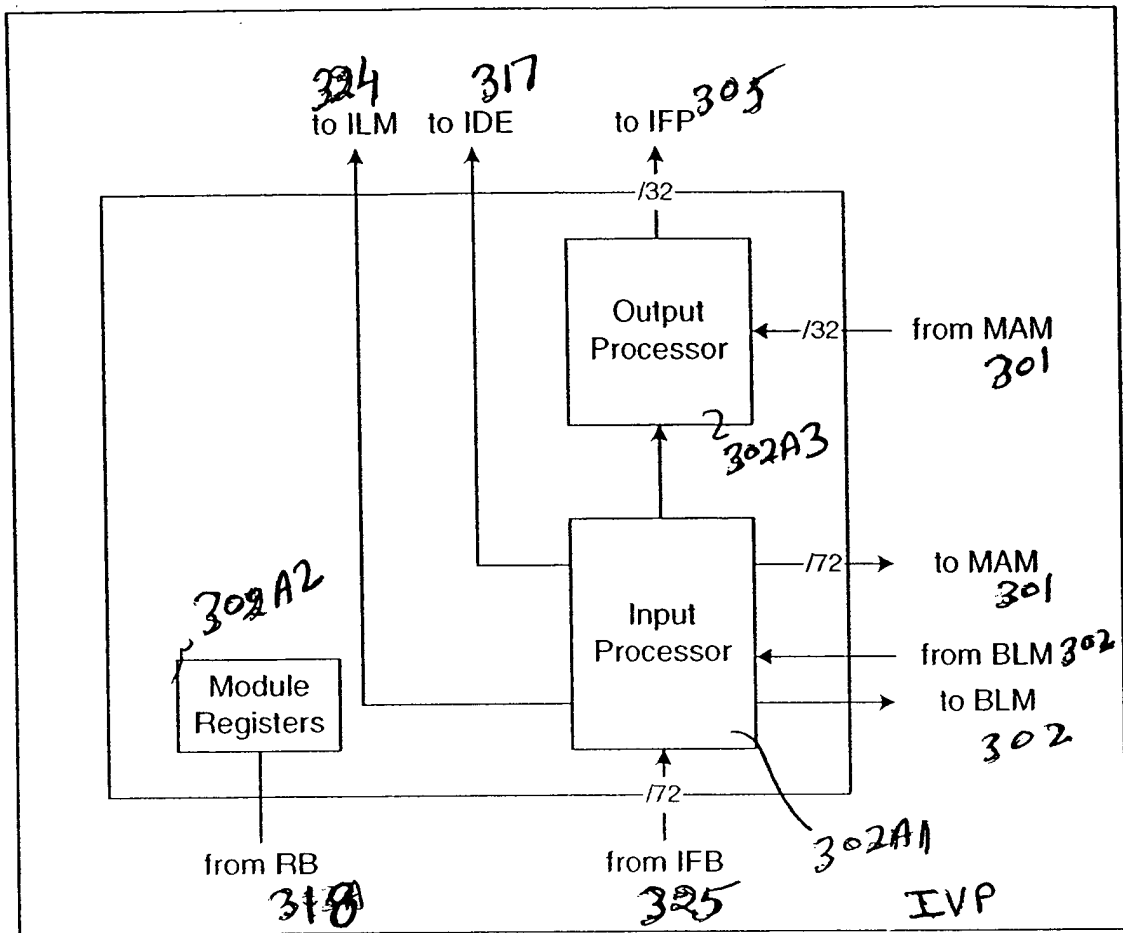


Figure 3J1

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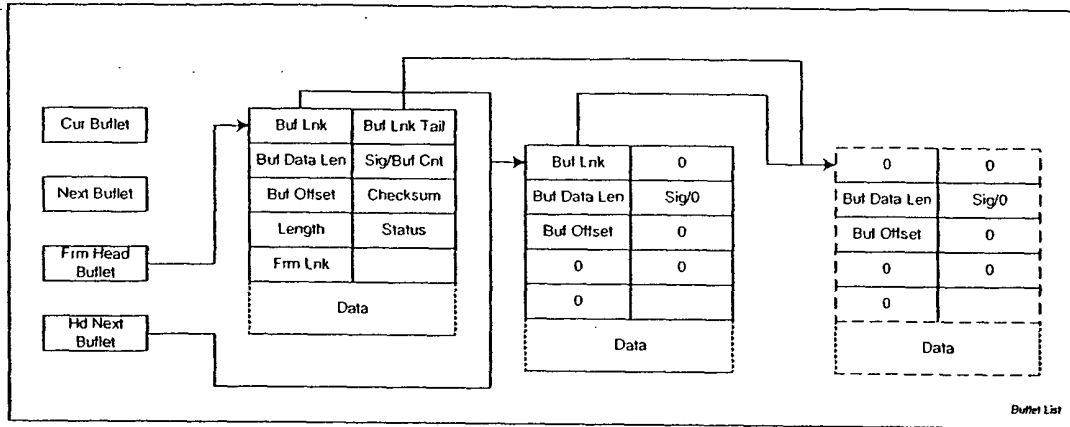


Figure 3J1 Buffer List Structure

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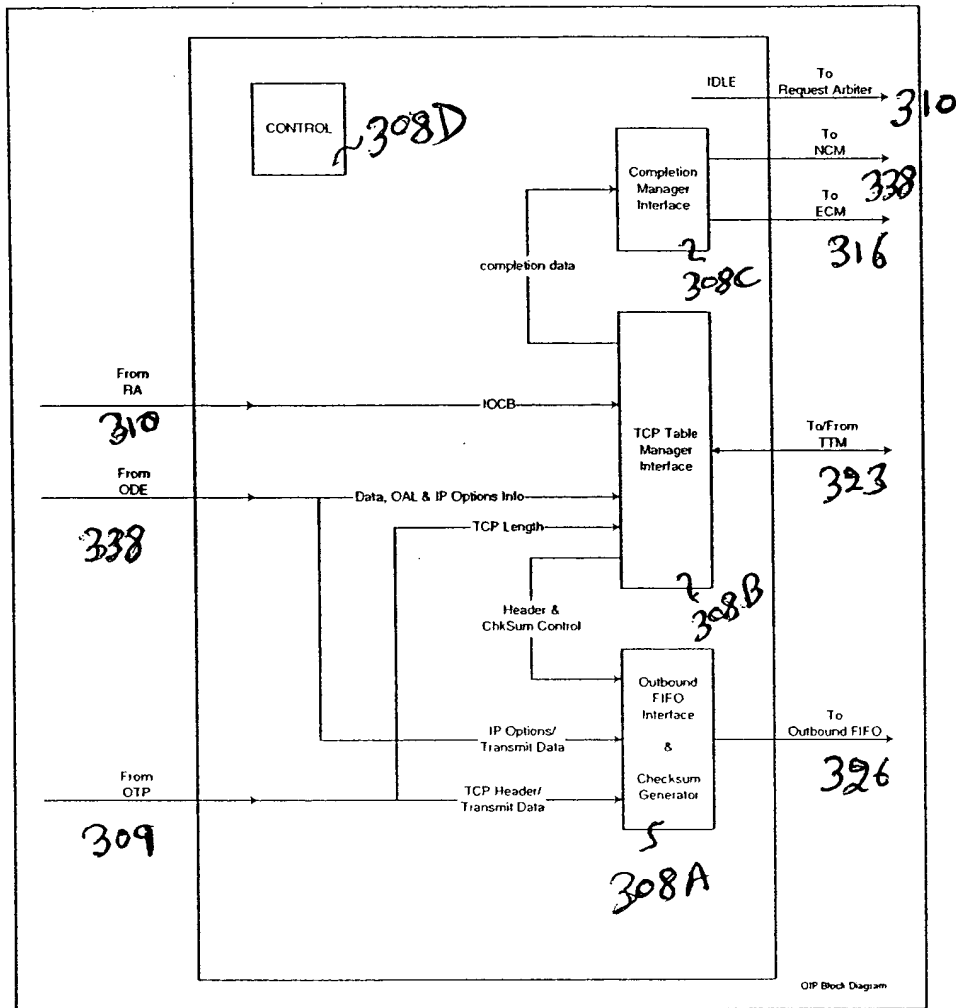


Figure 3K

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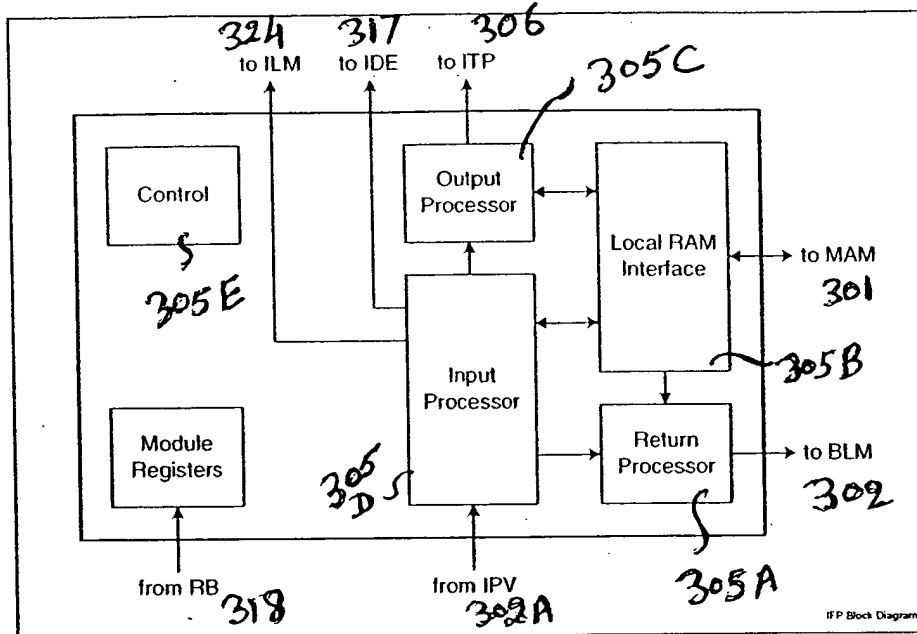


Figure 3L

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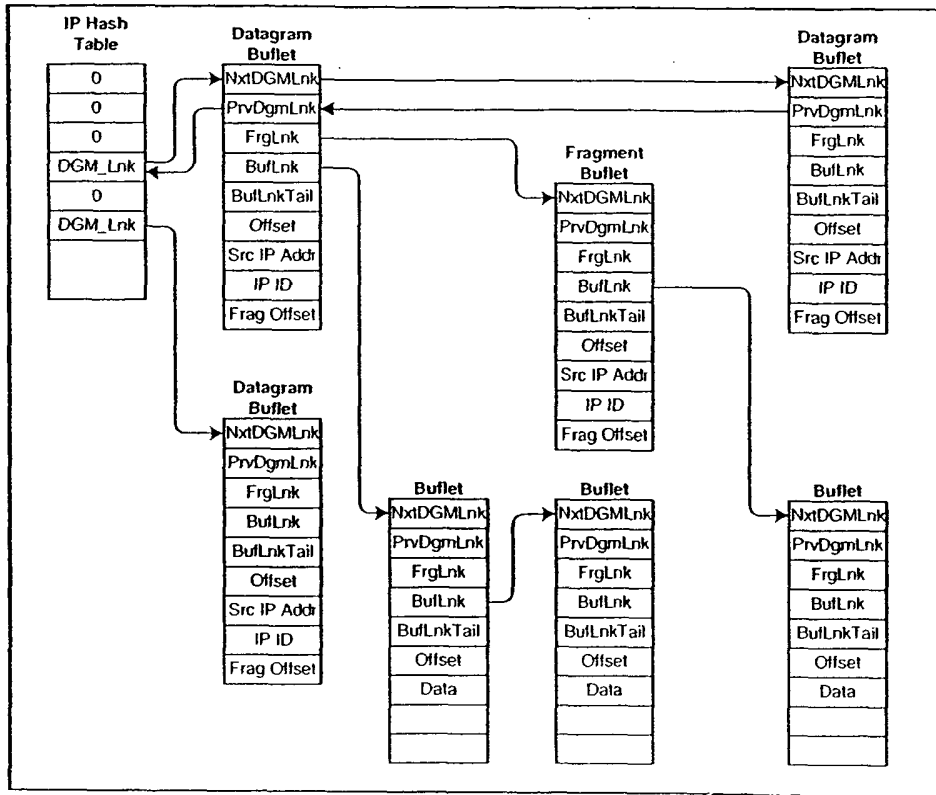


Figure 3L1:

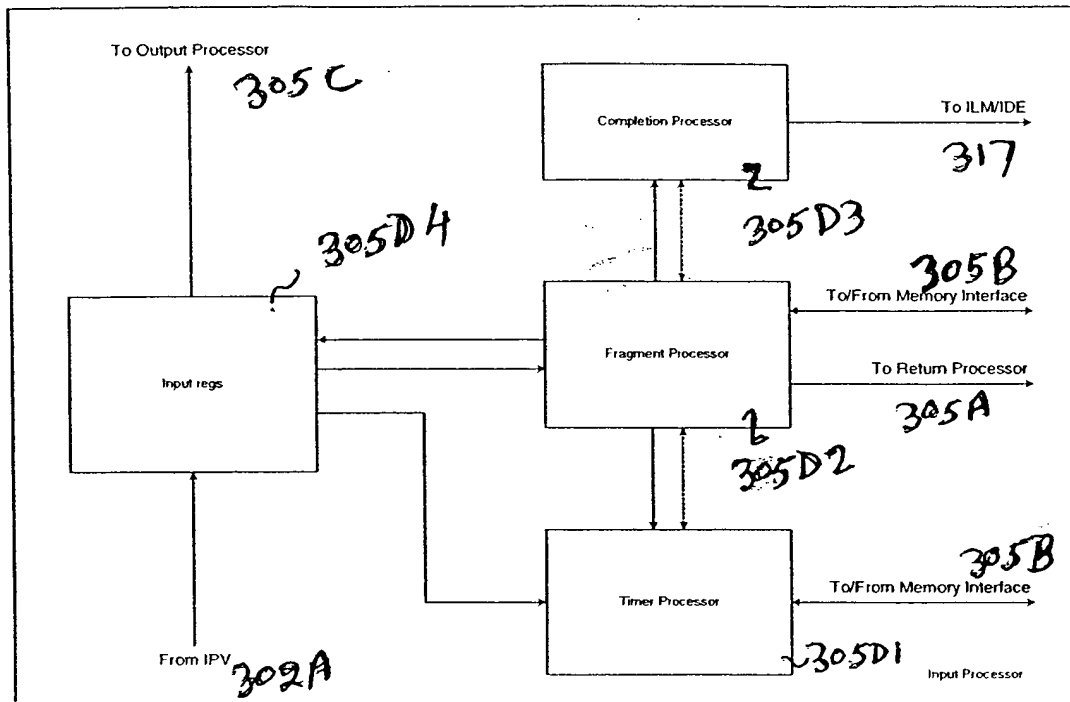


Figure 3L2

305D

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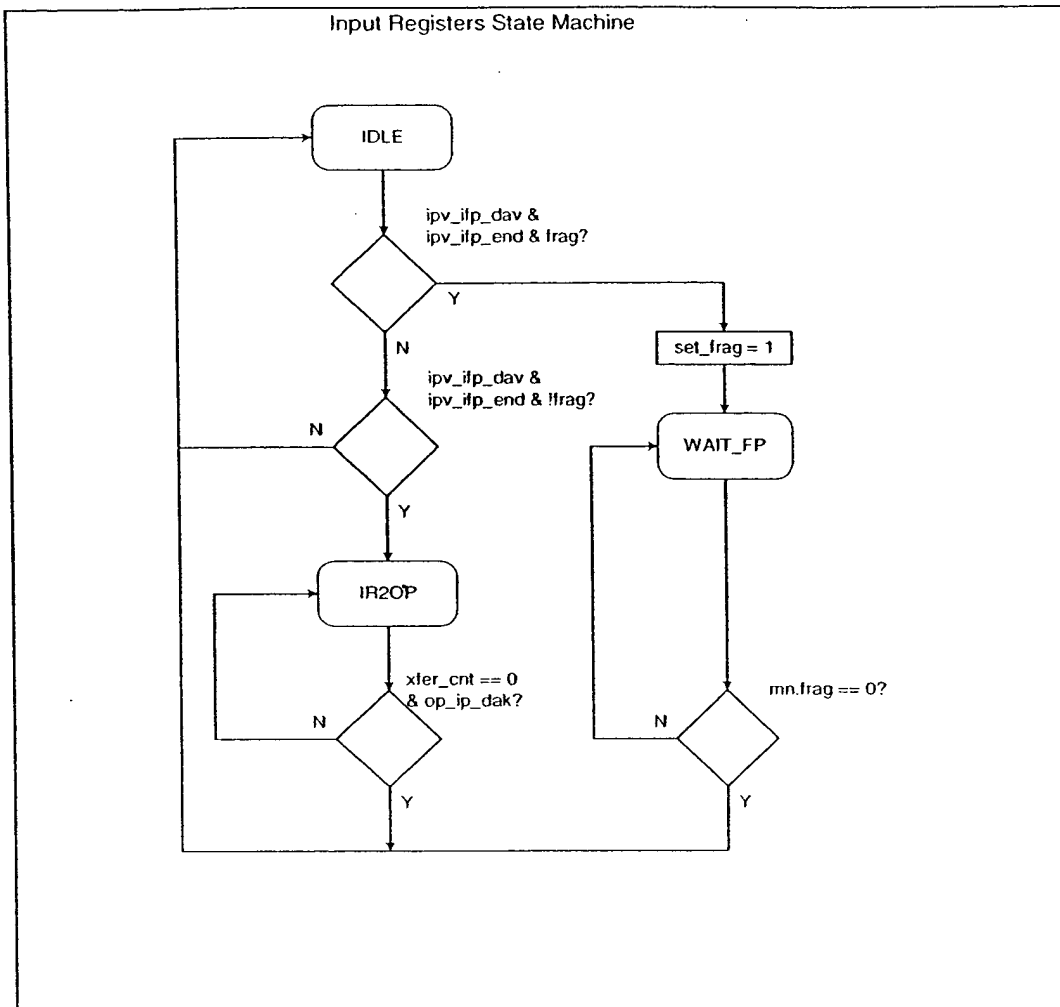


Figure 3L3

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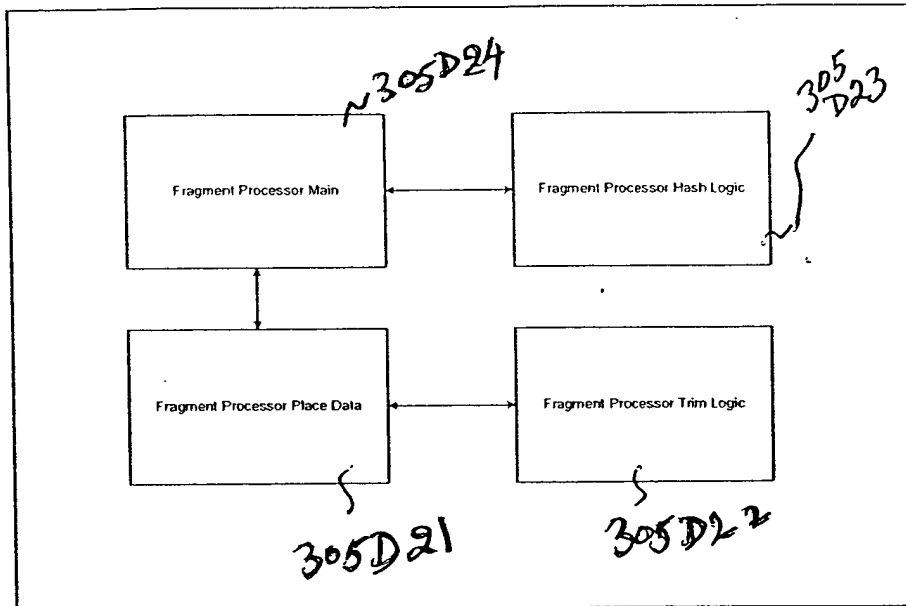


Figure 3L4

*305D2*

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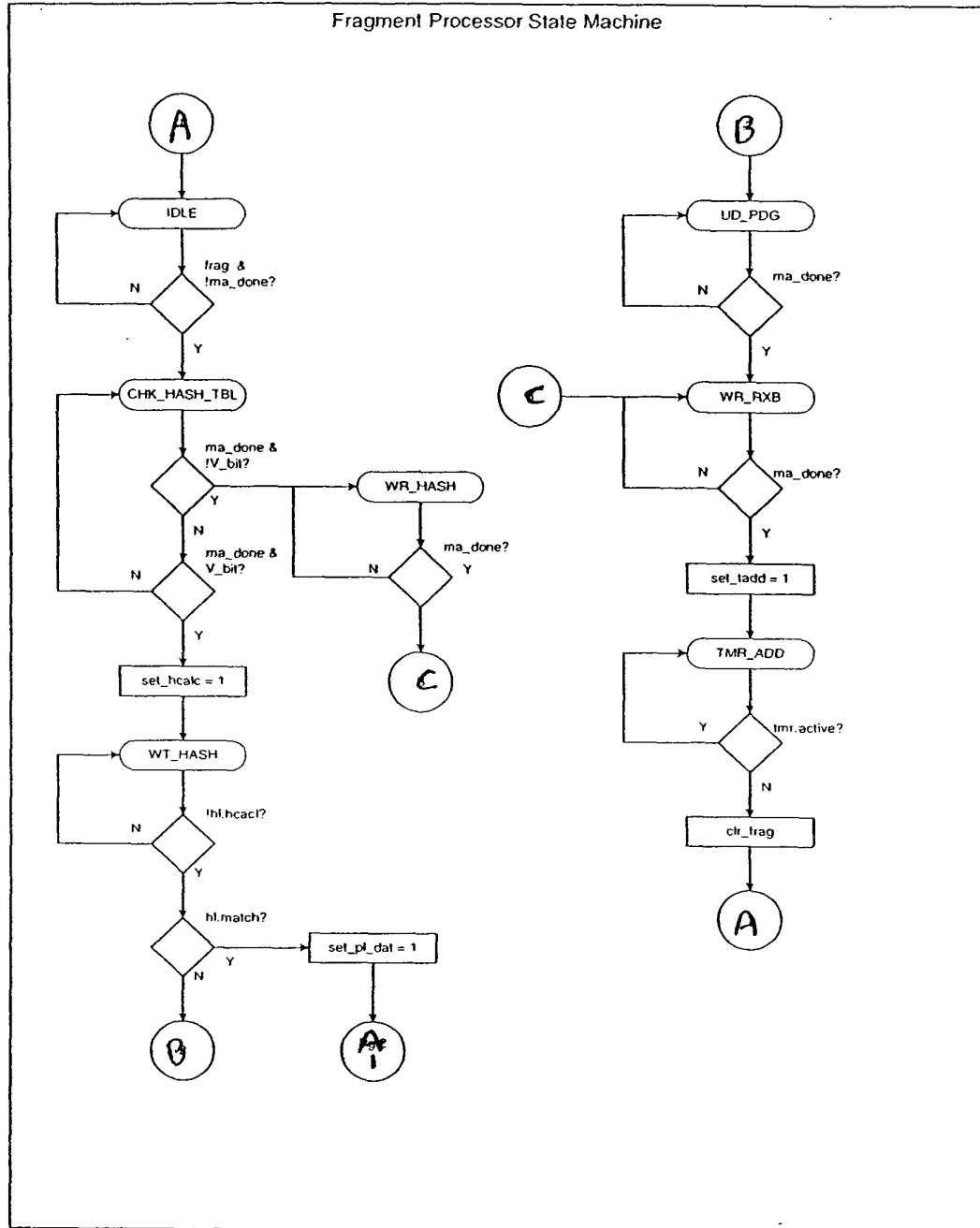


Figure 3L5A I

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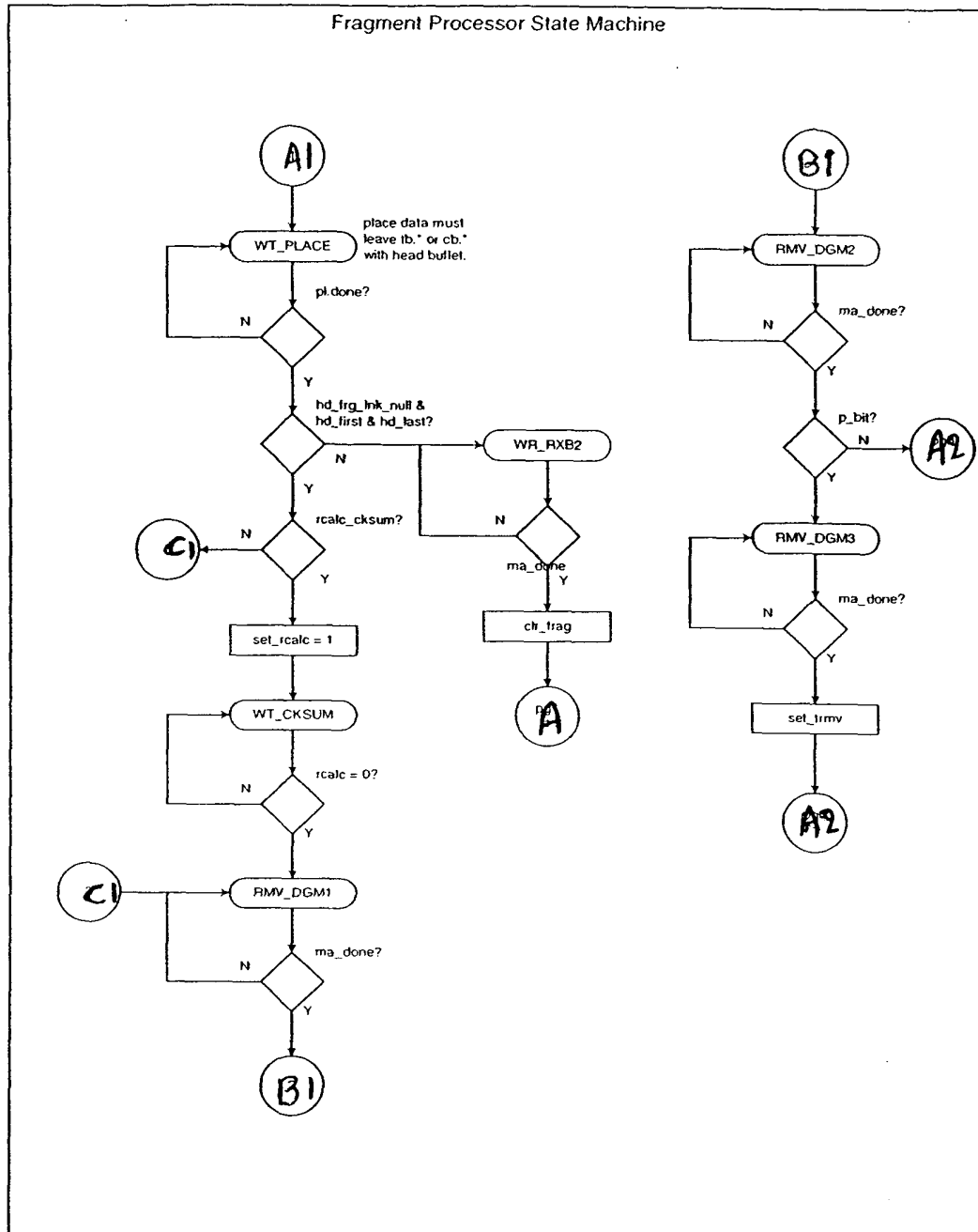


Figure 3L5B F

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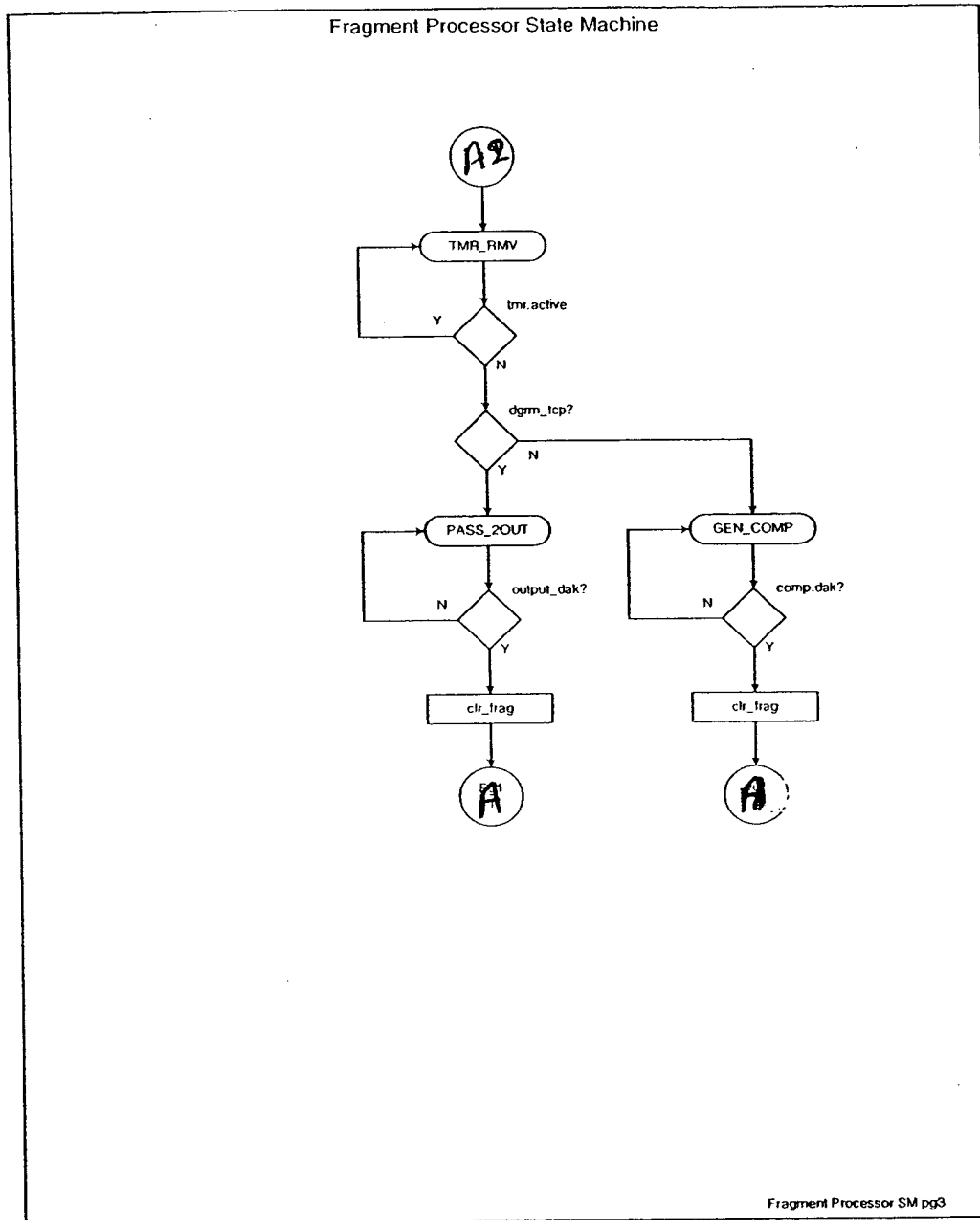


Figure 3L5C

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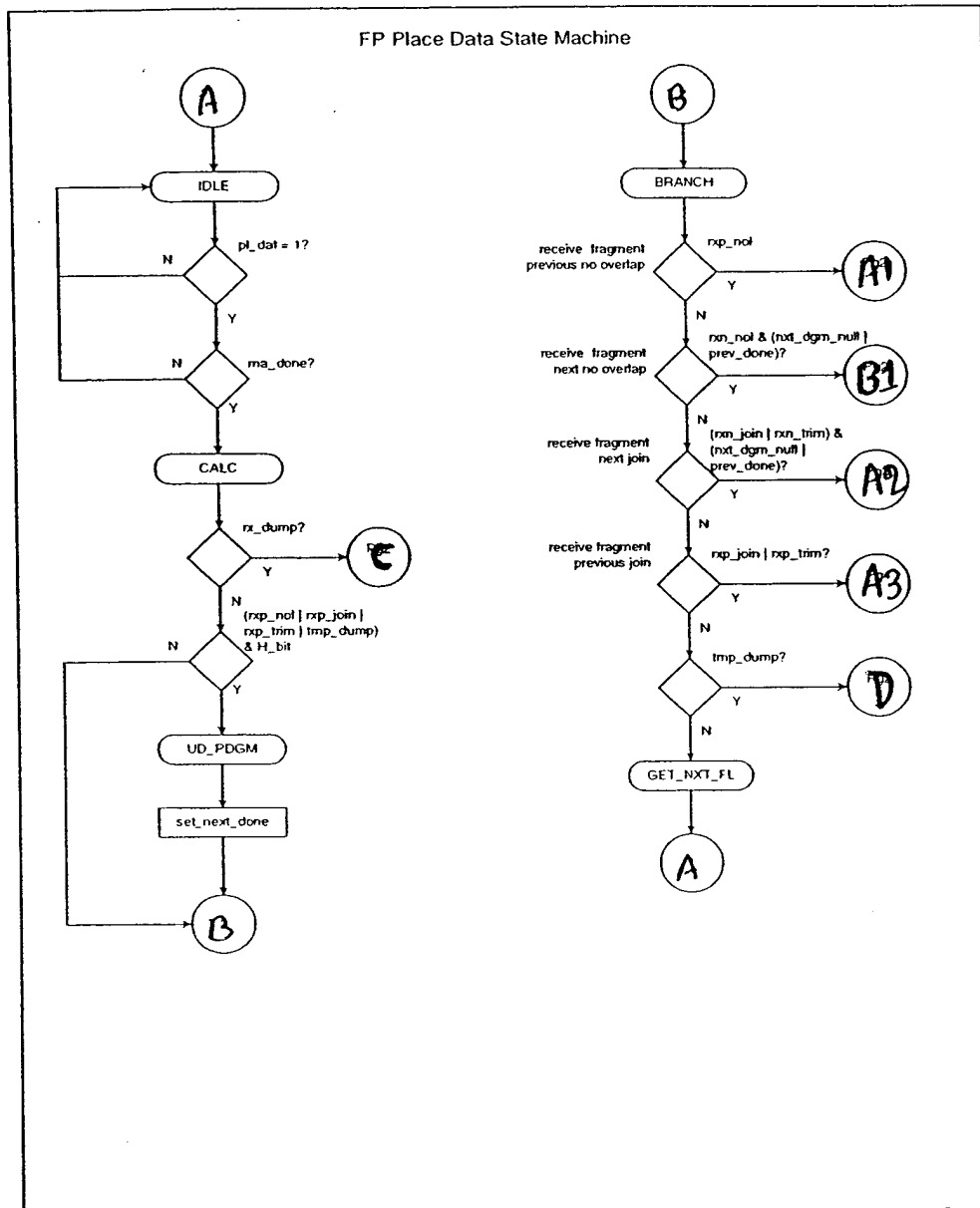


Figure 3L6A



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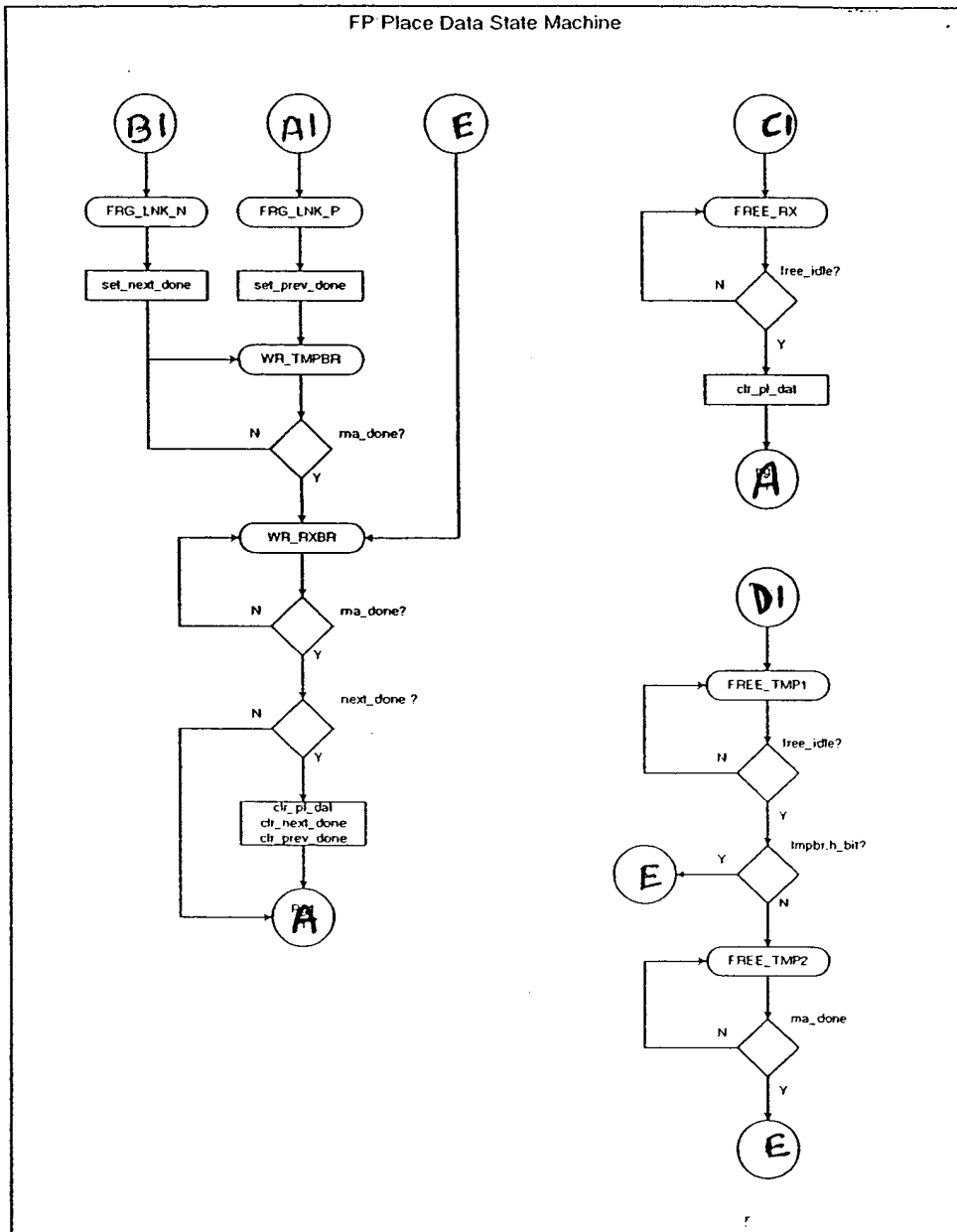


Figure 3L6 B

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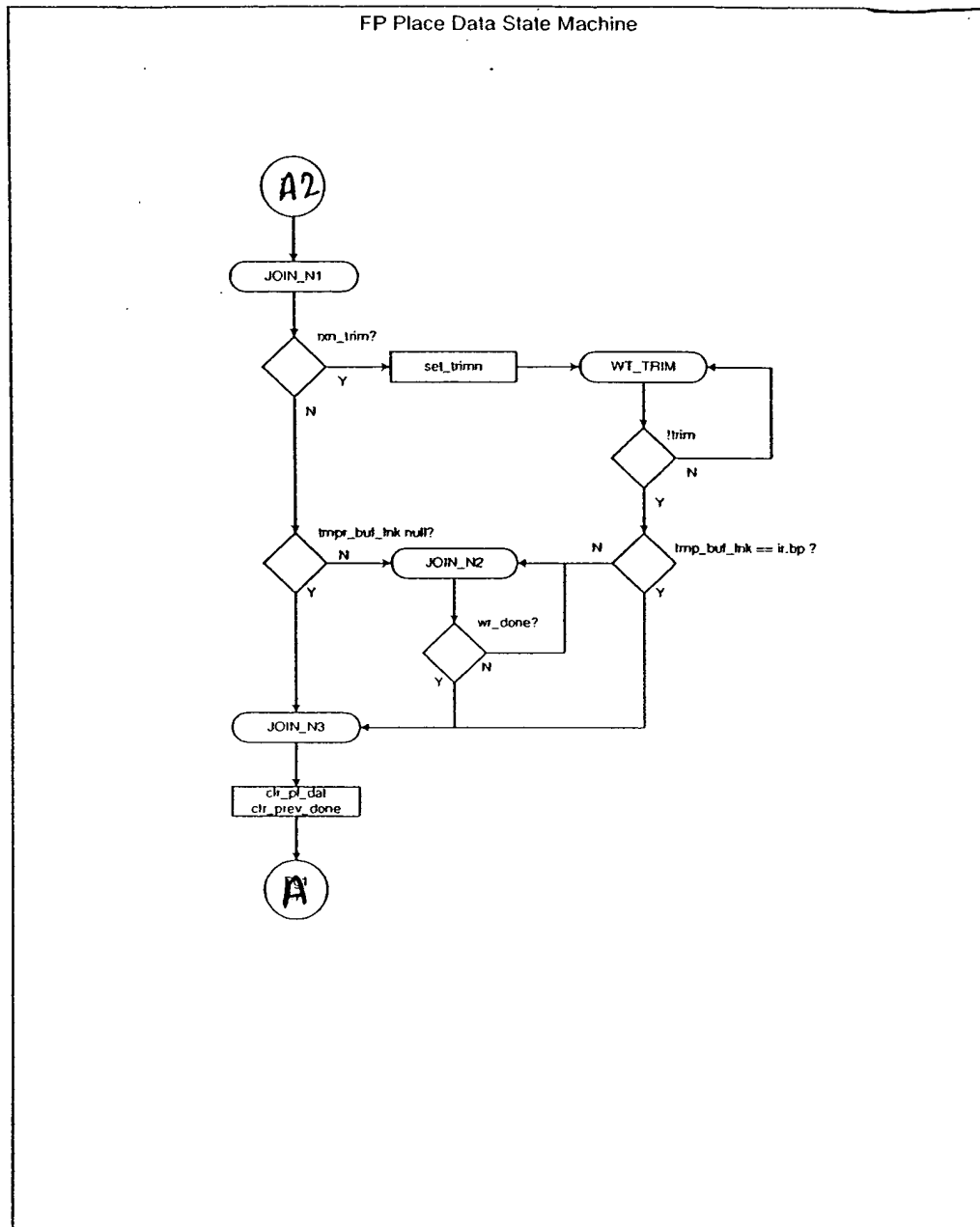


Figure 3L6C

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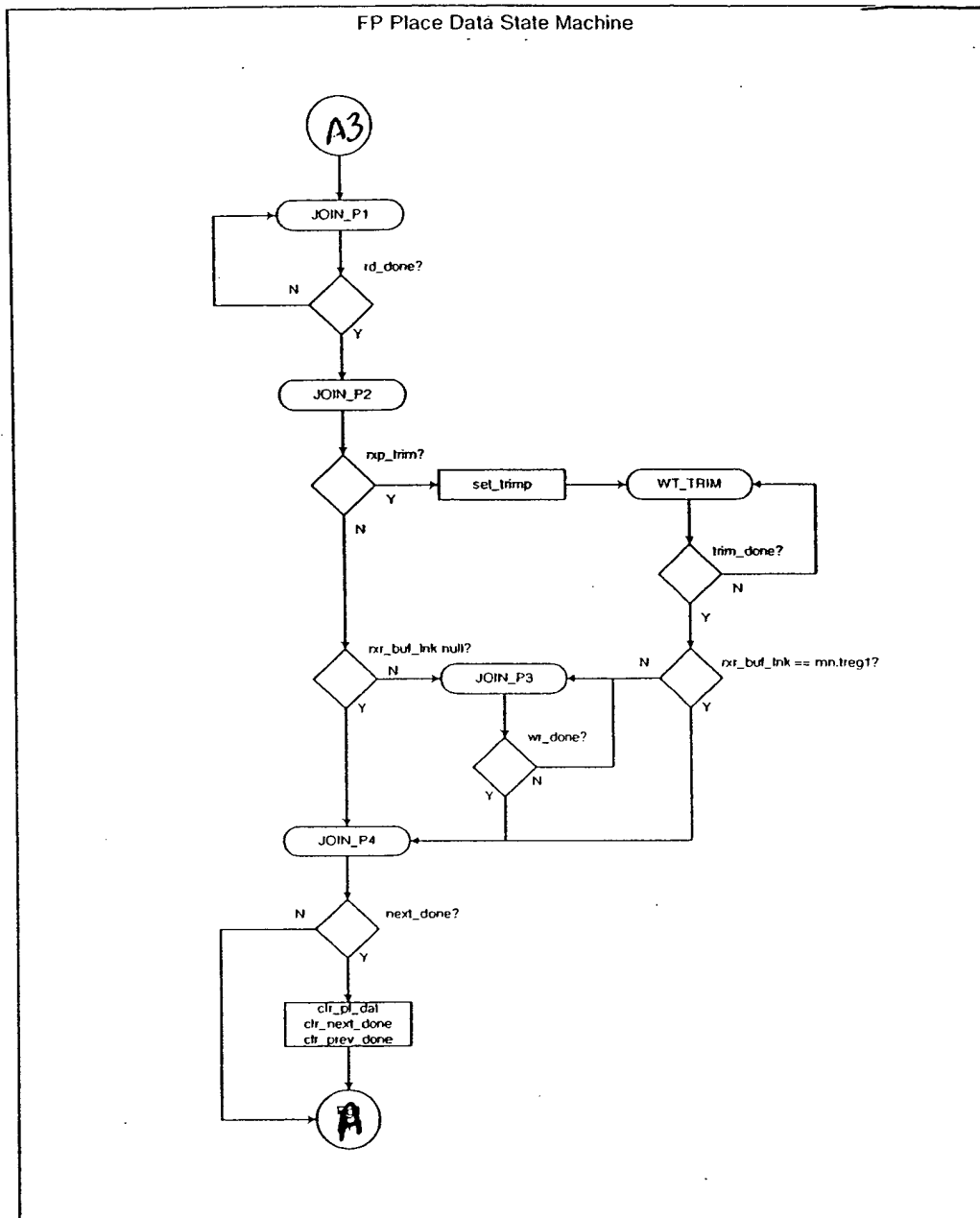


Figure 3L6D

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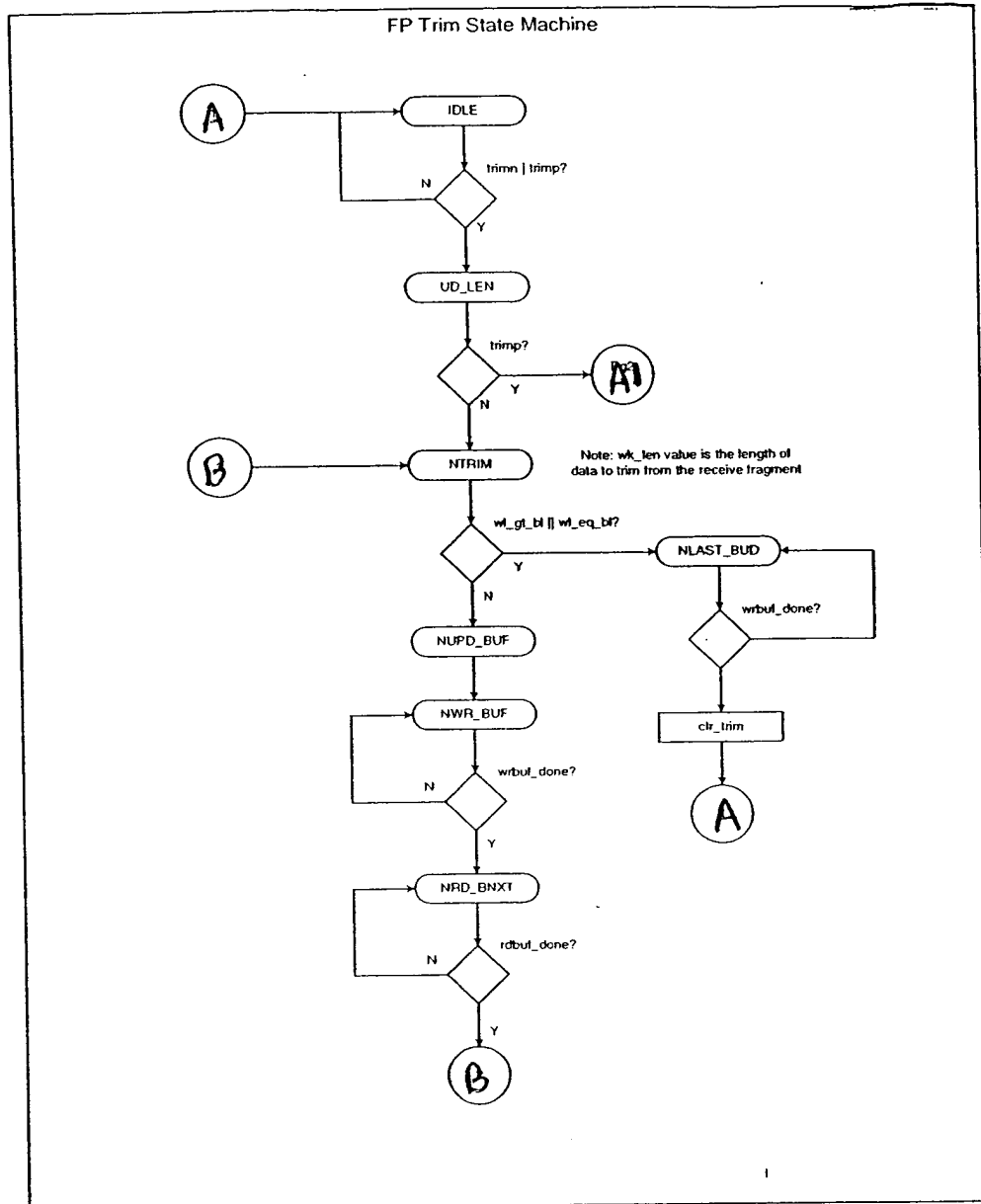


Figure 3L7A

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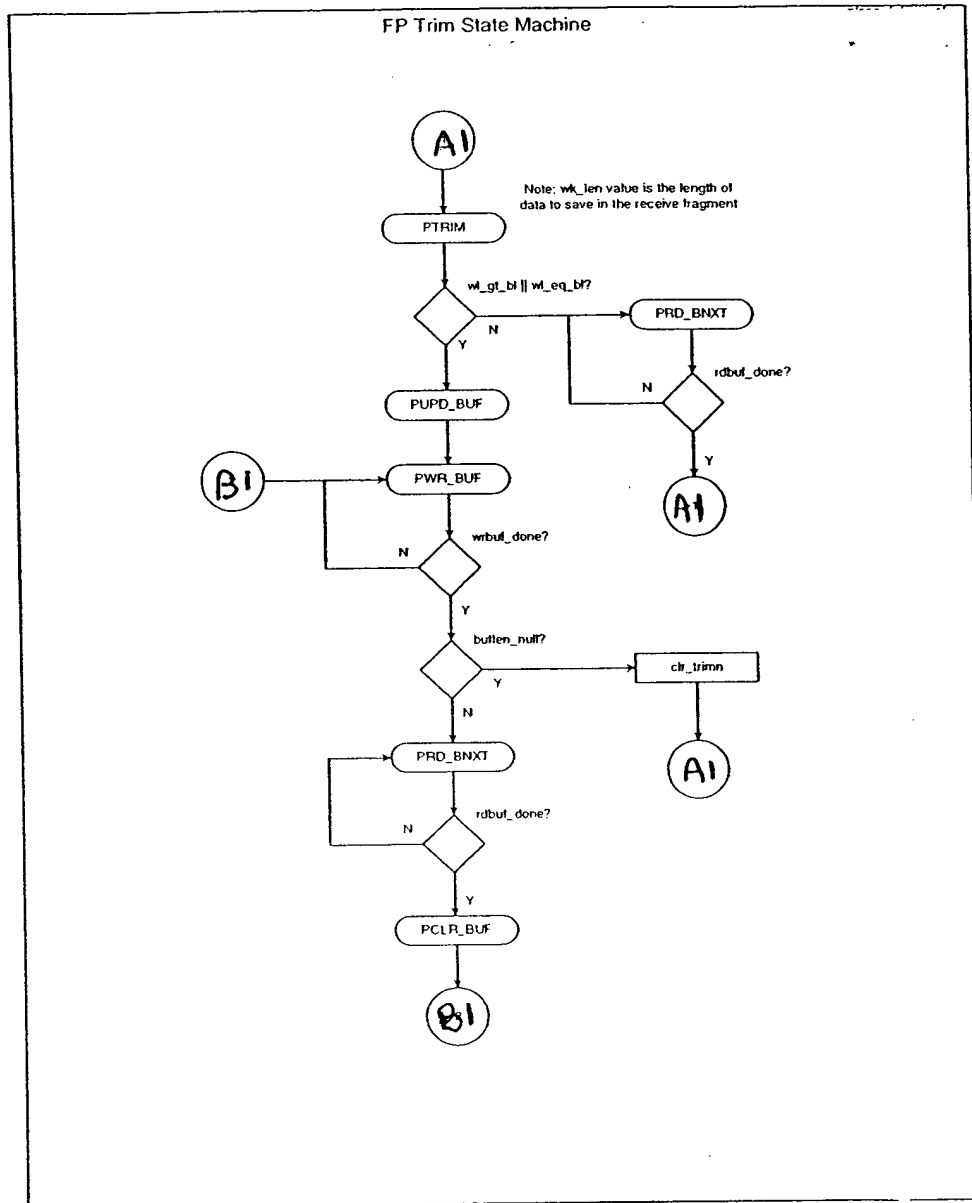


Figure 3L7B:

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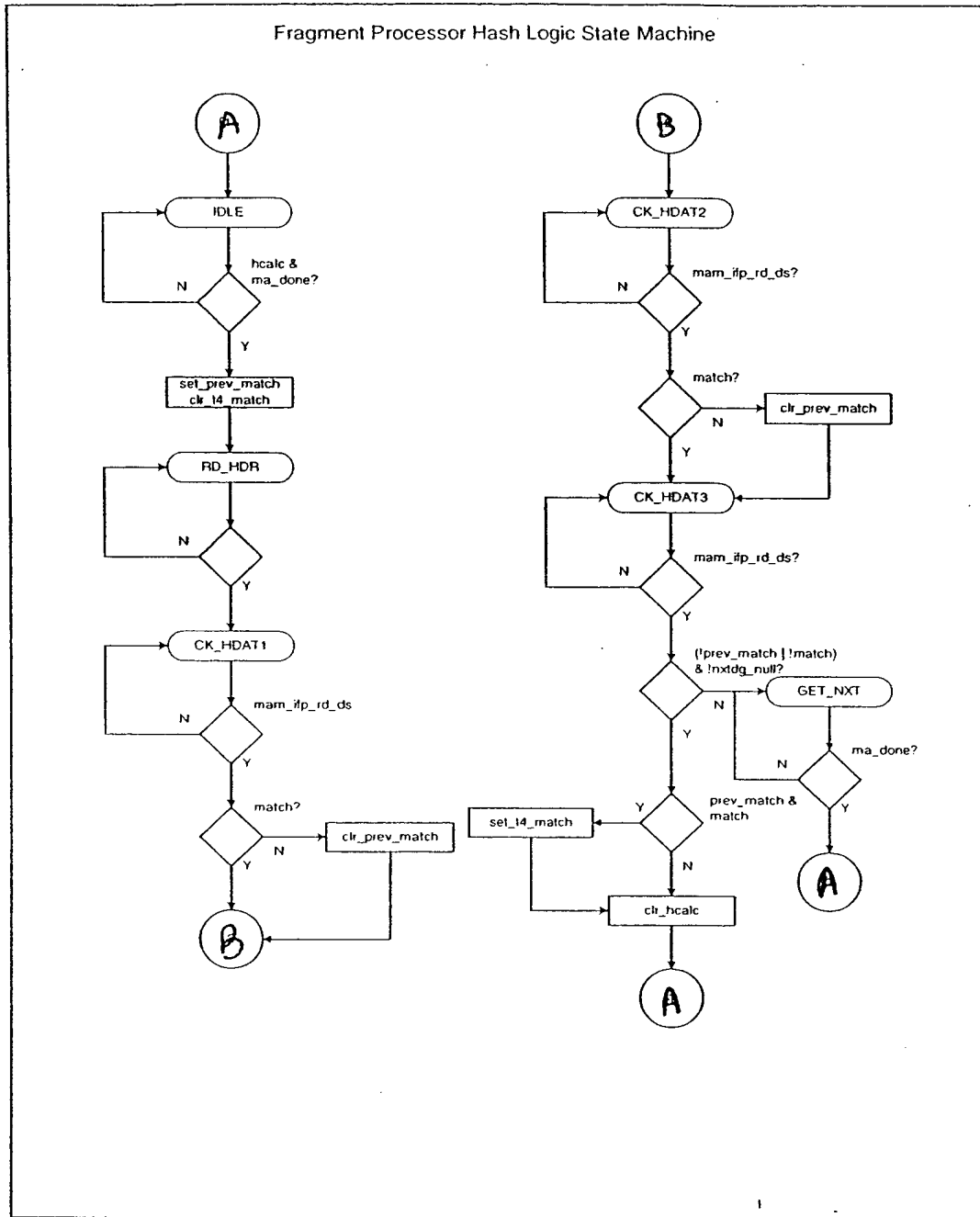


Figure 3L8 I

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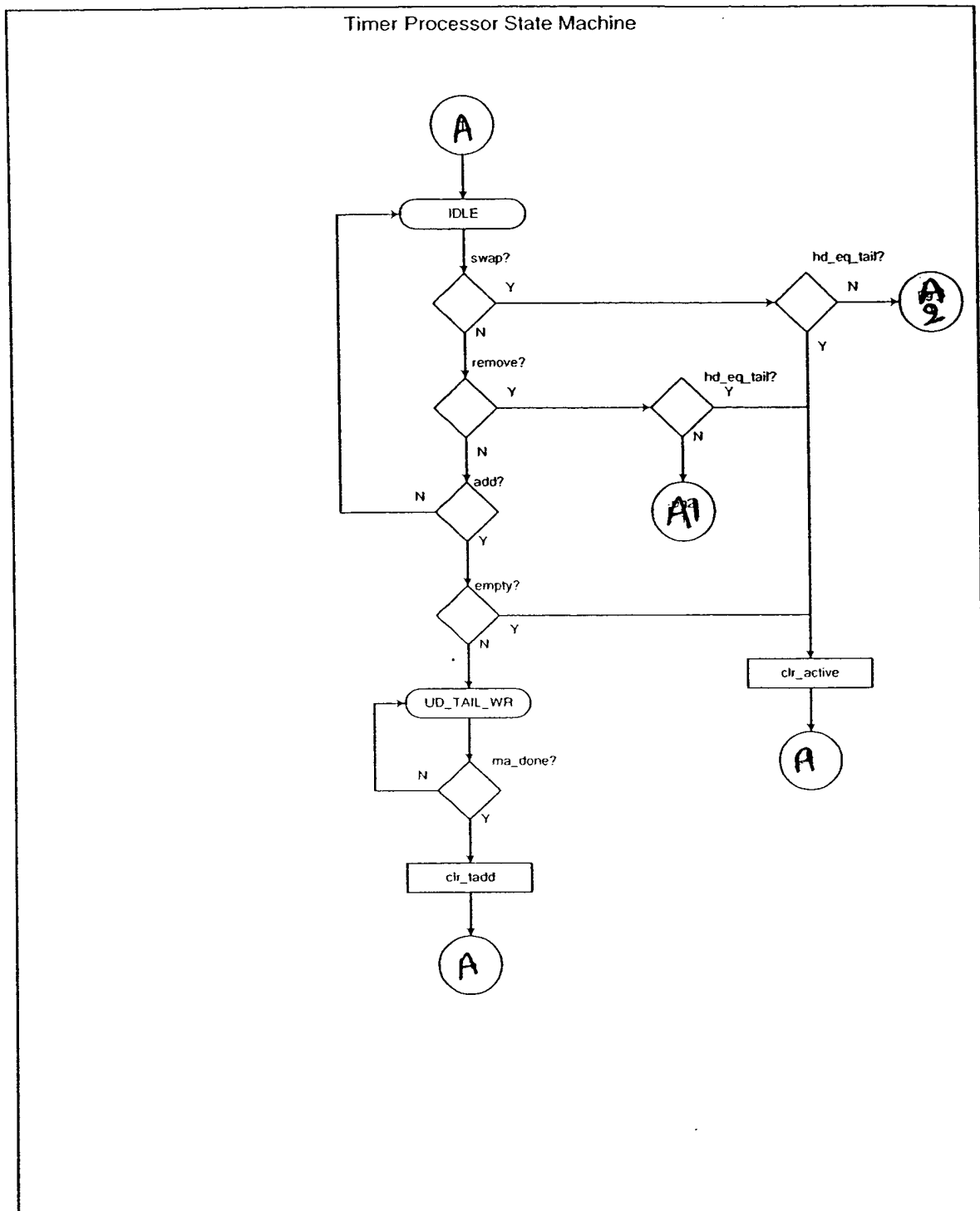


Figure 3L9A

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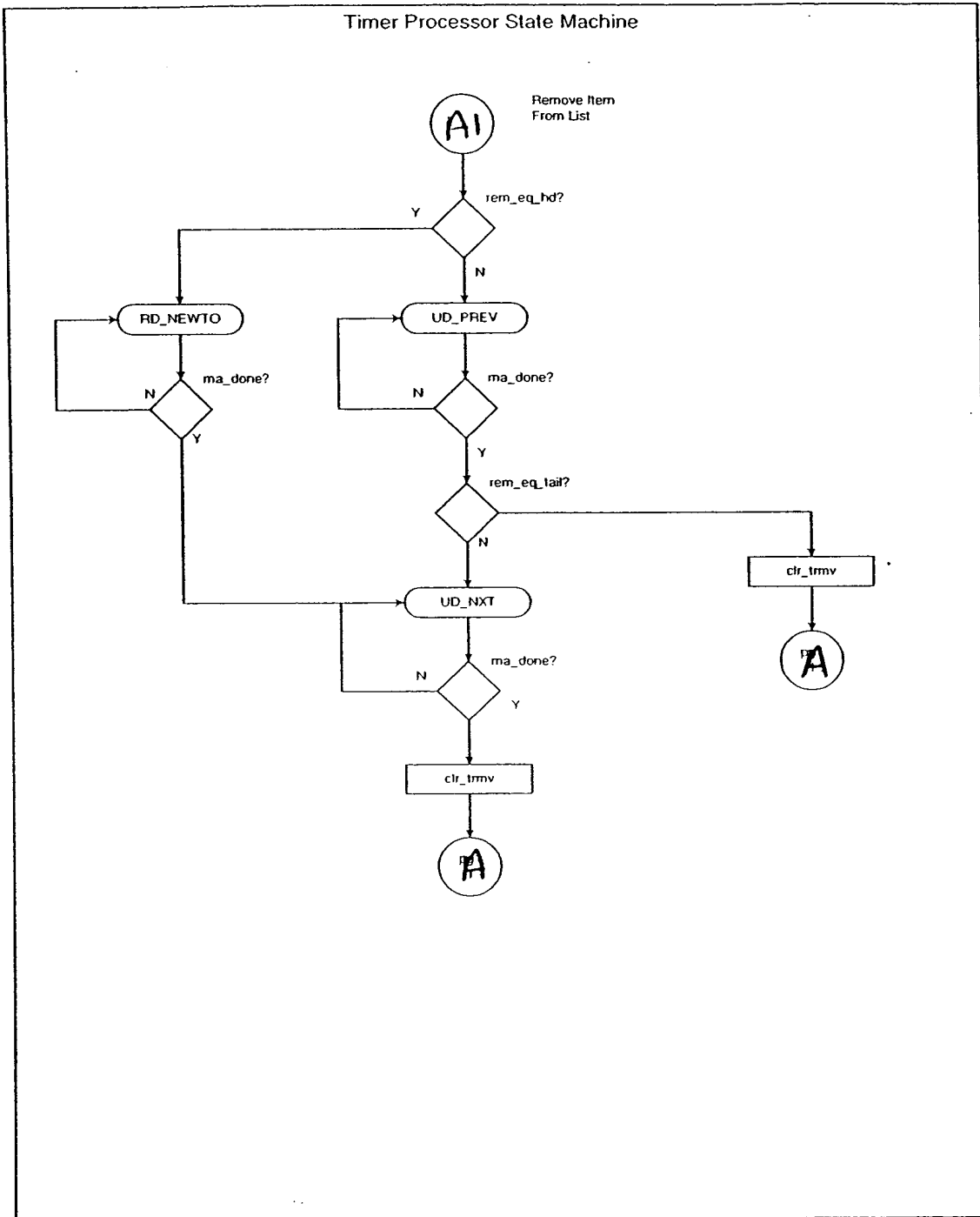


Figure 3L9B:



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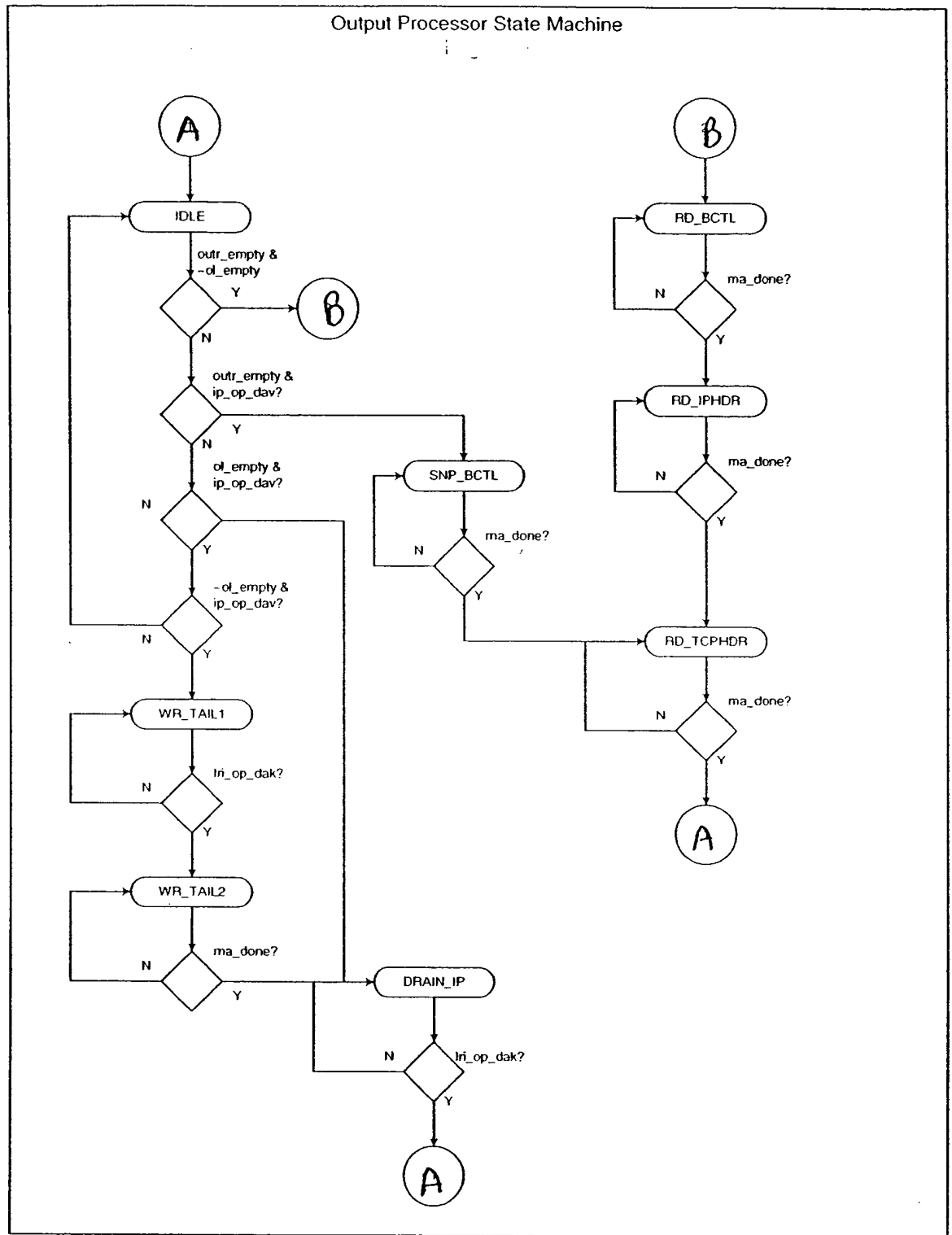


Figure 3L10 -

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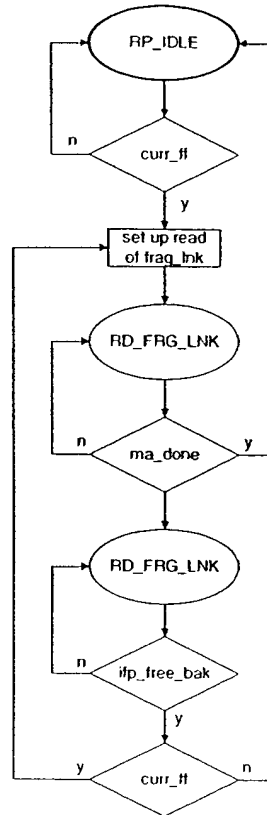


Figure 3L11 - Return Processor State Machine

404

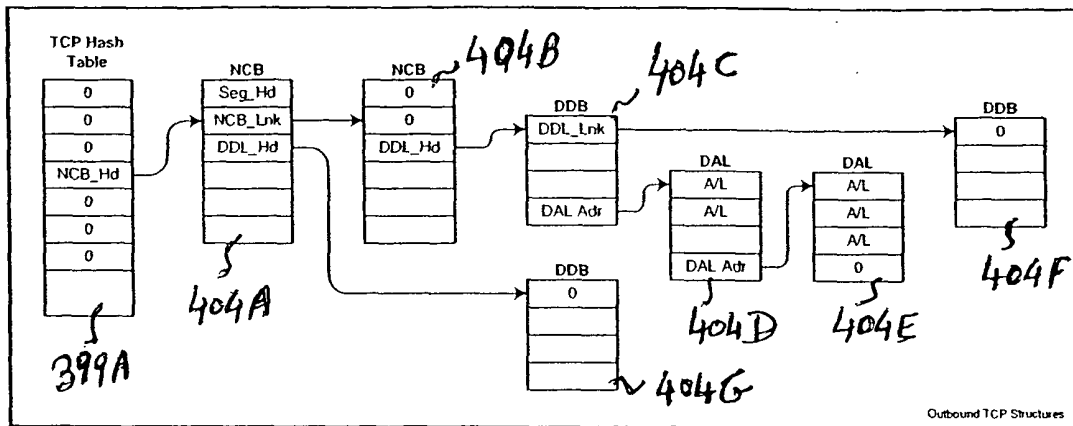


Figure 4A

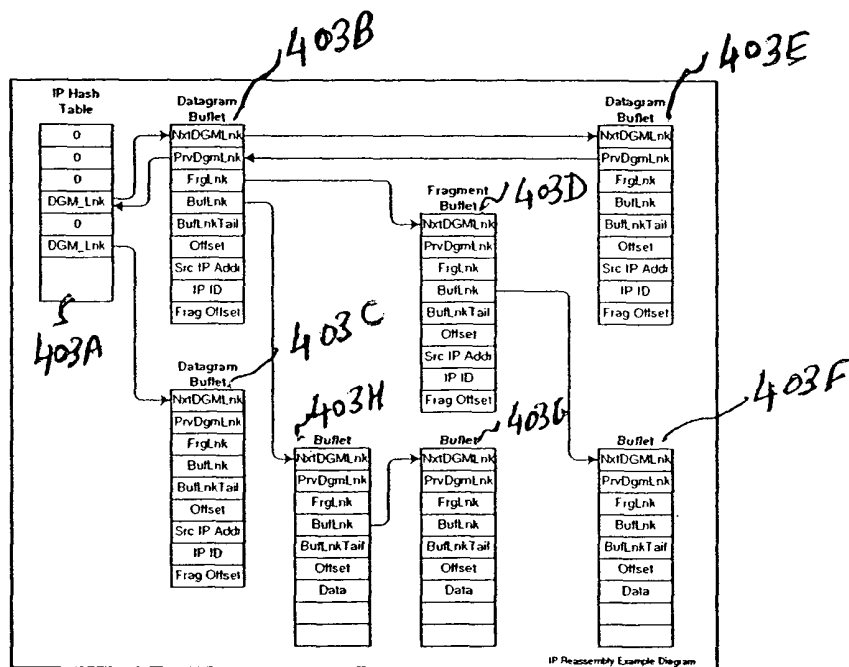


Figure 4B

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